

## Product Specification

### 4.25 Gb/s RoHS Compliant Short-Wavelength SFP Transceiver

#### FTLF8524P2xNy

#### PRODUCT FEATURES

- Up to 4.25 Gb/s bi-directional data links
- Hot-pluggable SFP footprint
- Built-in digital diagnostic functions
- 850nm Oxide VCSEL laser transmitter
- Duplex LC connector
- RoHS Compliant and Lead Free
- Up to 500m on 50/125µm MMF, 300m on 62.5/125µm MMF
- Metal enclosure, for lower EMI
- Single 3.3V power supply
- Extended operating temperature range: -20°C to 85°C



#### APPLICATIONS

- Tri-Rate 1.063/2.125/4.25 Gb/s Fibre Channel
- 1.25 Gb/s 1000Base-SX Ethernet (Rate selectable version)
- Wireless – OBSAI, CPRI

Finisar's FTLF8524P2xNy Small Form Factor Pluggable (SFP) transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA)<sup>1</sup>. They are compatible with Fibre Channel FC-PI-2 Rev. 7.0<sup>2</sup>. Rate Selectable versions are also compatible with Gigabit Ethernet as specified in IEEE Std 802.3<sup>3</sup>. They are RoHS compliant and lead-free per Directive 2002/95/EC<sup>4</sup> and Finisar Application Note AN-2038. Digital diagnostics functions are available via the 2-wire serial bus specified in the SFP MSA.

#### PRODUCT SELECTION

#### FTLF8524P2xNy

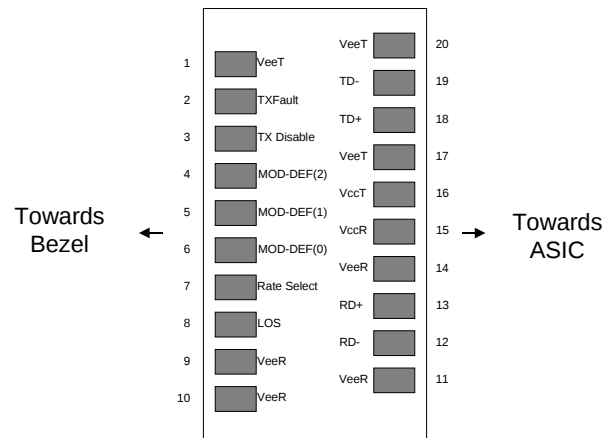
|   |   |                                                                 |
|---|---|-----------------------------------------------------------------|
| x | W | Wide Extraction Bail                                            |
|   | B | Narrow Extraction Bail                                          |
| y | L | Non Rate Selectable - 1x, 2x, 4x Fibre Channel                  |
|   | V | Rate Selectable - 1x, 2x, 4x Fibre Channel and Gigabit Ethernet |

**I. Pin Descriptions**

| Pin | Symbol             | Name/Description                                                                                                                                             | Ref. |
|-----|--------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|------|
| 1   | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                                             | 1    |
| 2   | T <sub>FAULT</sub> | Transmitter Fault.                                                                                                                                           |      |
| 3   | T <sub>DIS</sub>   | Transmitter Disable. Laser output disabled on high or open.                                                                                                  | 2    |
| 4   | MOD_DEF(2)         | Module Definition 2. Data line for Serial ID.                                                                                                                | 3    |
| 5   | MOD_DEF(1)         | Module Definition 1. Clock line for Serial ID.                                                                                                               | 3    |
| 6   | MOD_DEF(0)         | Module Definition 0. Grounded within the module.                                                                                                             | 3    |
| 7   | Rate Select        | Open or Low = 1.063 Gb/s or 2.125 Gb/s Fibre Channel, 1.25 Gb/s Gigabit Ethernet (Low Bandwidth)<br>High = 2.125 or 4.25 Gb/s Fibre Channel (High Bandwidth) | 4    |
| 8   | LOS                | Loss of Signal indication. Logic 0 indicates normal operation.                                                                                               | 5    |
| 9   | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                                             | 1    |
| 10  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                                             | 1    |
| 11  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                                             | 1    |
| 12  | RD-                | Receiver Inverted DATA out. AC Coupled                                                                                                                       |      |
| 13  | RD+                | Receiver Non-inverted DATA out. AC Coupled                                                                                                                   |      |
| 14  | V <sub>EER</sub>   | Receiver Ground (Common with Transmitter Ground)                                                                                                             | 1    |
| 15  | V <sub>CCR</sub>   | Receiver Power Supply                                                                                                                                        |      |
| 16  | V <sub>CCT</sub>   | Transmitter Power Supply                                                                                                                                     |      |
| 17  | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                                             | 1    |
| 18  | TD+                | Transmitter Non-Inverted DATA in. AC Coupled.                                                                                                                |      |
| 19  | TD-                | Transmitter Inverted DATA in. AC Coupled.                                                                                                                    |      |
| 20  | V <sub>EET</sub>   | Transmitter Ground (Common with Receiver Ground)                                                                                                             | 1    |

**Notes:**

1. Circuit ground is internally isolated from chassis ground.
2. Laser output disabled on T<sub>DIS</sub> > 2.0V or open, enabled on T<sub>DIS</sub> < 0.8V.
3. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD\_DEF(0) pulls line low to indicate module is plugged in.
4. **For Rate Selectable version only:** In accordance with SFF Committee SFF-8079 Draft Rev. 1.6, Table 3. Note that rate select can also be set through 2-wire bus in accordance with SFF-8472<sup>5</sup> at Bit 3, Byte 110, Address A2h (note: writing '1' selects full bandwidth operation). Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus. Non Rate Selectable version can operate at 1x, 2x, 4x Fibre Channel independent of rate select pin setting.
5. LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

**Diagram of Host Board Connector Block Pin Numbers and Names**

**II. Absolute Maximum Ratings**

| Parameter                  | Symbol          | Min  | Typ | Max | Unit | Ref. |
|----------------------------|-----------------|------|-----|-----|------|------|
| Maximum Supply Voltage     | V <sub>CC</sub> | -0.5 |     | 4.0 | V    |      |
| Storage Temperature        | T <sub>S</sub>  | -40  |     | 85  | °C   |      |
| Case Operating Temperature | T <sub>A</sub>  | -20  |     | 85  | °C   |      |
| Relative Humidity          | RH              | 0    |     | 85  | %    | 1    |

**III. Electrical Characteristics (T<sub>A</sub> = -20 to 85 °C, V<sub>CC</sub> = 3.0 to 3.6 Volts)**

| Parameter                                      | Symbol                 | Min             | Typ | Max                   | Unit | Ref. |
|------------------------------------------------|------------------------|-----------------|-----|-----------------------|------|------|
| Supply Voltage                                 | V <sub>CC</sub>        | 3.0             |     | 3.6                   | V    |      |
| Supply Current                                 | I <sub>CC</sub>        |                 | 180 | 240                   | mA   |      |
| <b>Transmitter</b>                             |                        |                 |     |                       |      |      |
| Input differential impedance                   | R <sub>in</sub>        |                 | 100 |                       | Ω    | 2    |
| Single ended data input swing                  | V <sub>in,pp</sub>     | 150             |     | 1200                  | mV   | 3    |
| Transmit Disable Voltage                       | V <sub>D</sub>         | 2               |     | V <sub>CC</sub>       | V    | 4    |
| Transmit Enable Voltage                        | V <sub>EN</sub>        | V <sub>EE</sub> |     | V <sub>EE</sub> + 0.8 | V    |      |
| <b>Receiver</b>                                |                        |                 |     |                       |      |      |
| Single ended data output swing                 | V <sub>out,pp</sub>    | 300             | 350 | 550                   | mV   | 5    |
| Data output rise time                          | t <sub>r</sub>         |                 |     | 120                   | ps   | 6    |
| Data output fall time                          | t <sub>f</sub>         |                 |     | 120                   | ps   | 6    |
| Mask Margin                                    |                        |                 | 40% |                       |      |      |
| LOS Fault                                      | V <sub>LOS fault</sub> | 2               |     | V <sub>CCHOST</sub>   | V    | 7    |
| LOS Normal                                     | V <sub>LOS norm</sub>  | V <sub>EE</sub> |     | V <sub>EE</sub> +0.8  | V    | 7    |
| Power Supply Rejection                         | PSR                    | 100             |     |                       | mVpp | 8    |
| Deterministic Jitter Contribution @ 2.125 Gb/s | RX ΔDJ                 |                 |     | 51.7                  | ps   | 9    |
| Deterministic Jitter Contribution @ 4.25 Gb/s  |                        |                 |     | 23.5                  | ps   | 9    |
| Total Jitter Contribution @ 2.125 Gb/s         | RX ΔTJ                 |                 |     | 122                   | ps   | 10   |
| Total Jitter Contribution @ 4.25 Gb/s          |                        |                 |     | 61                    | ps   | 10   |

**Notes:**

1. Non condensing.
2. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
3. We recommend <600mV for best EMI performance.
4. Or open circuit.
5. Into 100 ohms differential termination.
6. Unfiltered, 20 – 80 %
7. LOS is an open collector output. Should be pulled up with 4.7k – 10kohms on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.
8. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.
9. Typical peak-to-peak jitter (=6\*RMS width of Jitter).
10. Measured with DJ-free data input signal. In actual application, output DJ will be the sum of input DJ and ΔDJ. If measured with TJ-free data input signal. In actual application, output TJ will be given by:

$$TJ_{OUT} = DJ_{IN} + \Delta DJ + \sqrt{(TJ_{IN} - DJ_{IN})^2 + (\Delta TJ - \Delta DJ)^2}$$

**IV. Optical Characteristics (T<sub>OP</sub> = -20 to 85 °C, V<sub>CC</sub> = 3.00 to 3.60 Volts)**

| Parameter                                      | Symbol                          | Min   | Typ | Max   | Unit  | Ref. |
|------------------------------------------------|---------------------------------|-------|-----|-------|-------|------|
| <b>Transmitter</b>                             |                                 |       |     |       |       |      |
| Output Opt. Pwr: 50 or 62.5 MMF                | P <sub>OUT</sub>                | -9    |     | -2.5  | dBm   | 1    |
| Optical Wavelength                             | λ                               | 830   |     | 860   | nm    |      |
| Spectral Width                                 | σ                               |       |     | 0.85  | nm    |      |
| Optical Modulation Amplitude @ 4.25 Gb/s       | OMA                             | 247   |     |       | μW    | 2    |
| Optical Modulation Amplitude @ 2.125 Gb/s      | OMA                             | 196   |     |       | μW    | 2    |
| Optical Modulation Amplitude @ 1.0625 Gb/s     | OMA                             | 156   |     |       | μW    | 2    |
| Optical Rise/Fall Time                         | t <sub>r</sub> / t <sub>f</sub> |       |     | 90    | ps    | 3    |
| Relative Intensity Noise                       | RIN                             |       |     | -118  | dB/Hz |      |
| Deterministic Jitter Contribution @ 2.125 Gb/s | TX ΔDJ                          |       |     | 56.5  | ps    | 4    |
| Deterministic Jitter Contribution @ 4.25 Gb/s  | TX ΔDJ                          |       |     | 28.2  | ps    | 4    |
| Total Jitter Contribution @ 2.125 Gb/s         | TX ΔTJ                          |       |     | 119   | ps    | 5    |
| Total Jitter Contribution @ 4.25 Gb/s          | TX ΔTJ                          |       |     | 56.5  | ps    | 5    |
| Extinction Ratio @ 1.25 Gb/s                   | ER                              | 9     |     |       | dB    | 6    |
| Mask Margin                                    |                                 |       | 45% |       |       |      |
| <b>Receiver</b>                                |                                 |       |     |       |       |      |
| Receiver Sensitivity = 1.0625 Gb/s             | R <sub>x</sub> SENS             |       |     | -20   | dBm   | 7    |
| Receiver Sensitivity = 2.125 Gb/s              | R <sub>x</sub> SENS             |       |     | -18   | dBm   | 7    |
| Receiver Sensitivity = 4.25 Gb/s               | R <sub>x</sub> SENS             |       |     | -15   | dBm   | 7    |
| Receiver Sensitivity = 1.25 Gb/s               | R <sub>x</sub> SENS             |       |     | -20   | dBm   | 8    |
| Stressed RX sens. =1.0625 Gb/s                 |                                 | 0.055 |     |       | mW    | 9    |
| Stressed RX sens. =2.125 Gb/s                  |                                 | 0.096 |     |       | mW    | 9    |
| Stressed Rx sens. = 4.25 Gb/s                  |                                 | 0.138 |     |       | mW    | 9    |
| Stressed RX sens. =1.25 Gb/s                   |                                 |       |     | -13.5 | dBm   | 10   |
| Average Receiver Power                         | R <sub>x</sub> MAX              |       |     | 0     | dBm   |      |
| Receiver Elec. 3 dB cutoff frequency           |                                 |       |     | 1500  | MHz   | 11   |
| Receiver Elec. 3 dB cutoff frequency           |                                 |       |     | 2500  | MHz   | 12   |
| Optical Center Wavelength                      | λ <sub>C</sub>                  | 770   |     | 860   | nm    |      |
| Optical Return Loss                            |                                 | 12    |     |       | dB    |      |
| LOS De-Assert                                  | LOS <sub>D</sub>                |       |     | -20   | dBm   |      |
| LOS Assert                                     | LOS <sub>A</sub>                | -30   |     |       | dBm   |      |
| LOS Hysteresis                                 |                                 | 0.5   |     |       | dB    |      |

**Notes:**

- Class 1 Laser Safety per FDA/CDRH, and EN (IEC) 60825 laser safety standards.
- Equivalent extinction ratio specification for Fibre Channel. Allows smaller ER at higher average power.
- Unfiltered, 20-80%. Complies with FC 1x and 2x eye mask when filtered.
- Measured with DJ-free data input signal. In actual application, output DJ will be the sum of input DJ and ΔDJ.
- If measured with TJ-free data input signal. In actual application, output TJ will be given by:

$$TJ_{OUT} = DJ_{IN} + \Delta DJ + \sqrt{(TJ_{IN} - DJ_{IN})^2 + (\Delta TJ - \Delta DJ)^2}$$

- Applicable for Rate Selectable version only in low bandwidth mode.
- Specifications are for 50 micro-meter or 62.5 micro-meter fiber.
- As measured with 9dB extinction ratio.
- Measured with conformance signals defined in FC-PI-2 Rev. 7.0 specifications.

10. Measured with conformance signals defined in IEEE 802.3 specifications.
11. Rate Selectable version in low bandwidth mode.
12. Rate Selectable version in high bandwidth mode.

## V. General Specifications

| Parameter                      | Symbol | Min | Typ                               | Max        | Units  | Ref. |
|--------------------------------|--------|-----|-----------------------------------|------------|--------|------|
| Data Rate                      | BR     |     | 1.062,<br>1.25,<br>2.125,<br>4.25 |            | Gb/sec | 1    |
| Bit Error Rate                 | BER    |     |                                   | $10^{-12}$ |        | 5    |
| Fiber Length on 50/125µm MMF   | L      |     |                                   | 550        | m      | 2    |
|                                |        |     |                                   | 300        |        | 3    |
|                                |        |     |                                   | 150        |        | 4    |
| Fiber Length on 62.5/125µm MMF | L      |     |                                   | 300        | m      | 2    |
|                                |        |     |                                   | 150        |        | 3    |
|                                |        |     |                                   | 70         |        | 4    |

### Notes:

1. 1x, 2x,4x Fibre Channel compatible, per FC-P1-2 Rev. 7.0. Rate selectable version is also Gigabit Ethernet compatible per IEEE 802.3.
2. At 1.0625 Gb/s Fibre Channel data rate and, for rate selectable version, at 1.25 Gb/s Gigabit Ethernet data rate.
3. At 2.125 Gb/s Fibre Channel data rate.
4. At 4.25 Gb/s Fibre Channel data rate
5. 4.25Gb/s with PRBS  $2^7-1$ .

## VI. Environmental Specifications

Finisar 850nm SFP transceivers have an extended operating temperature range from –20°C to +85°C case temperature.

| Parameter                  | Symbol           | Min | Typ | Max | Units | Ref. |
|----------------------------|------------------|-----|-----|-----|-------|------|
| Case Operating Temperature | T <sub>op</sub>  | -20 |     | 85  | °C    |      |
| Storage Temperature        | T <sub>sto</sub> | -40 |     | 85  | °C    |      |

## VII. Regulatory Compliance

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

## VIII. Digital Diagnostic Functions

Finisar FTLF8524P2xNy SFP transceivers support the 2-wire serial communication protocol as defined in the SFP MSA<sup>1</sup>. It is very closely related to the E<sup>2</sup>PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP transceivers provide an enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E<sup>2</sup>PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E<sup>2</sup>PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation<sup>1,5</sup> and Finisar Application Note AN-2030.

Please note that evaluation board FDB-1018 is available with Finisar ModDEMO software that allows simple to use communication over the 2-wire serial interface.

**IX. Digital Diagnostic Specifications**

FTLF8524P2xNy transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

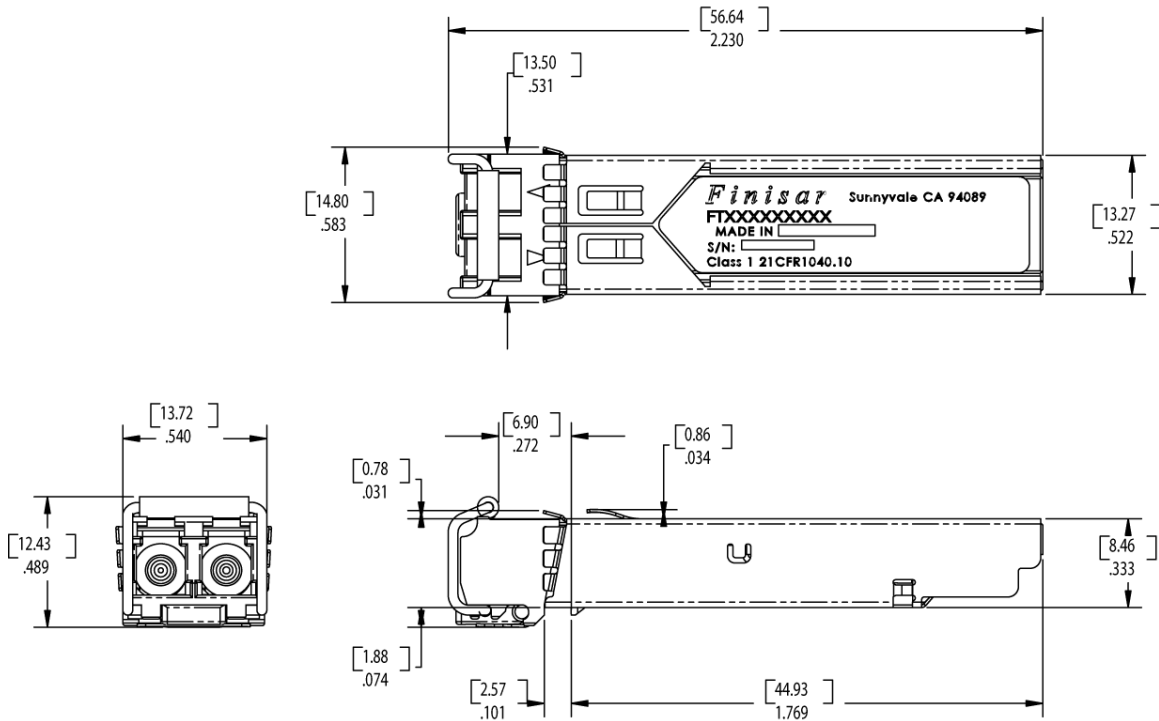
| Parameter                                      | Symbol                    | Min | Typ | Max  | Units | Ref. |
|------------------------------------------------|---------------------------|-----|-----|------|-------|------|
| <b>Accuracy</b>                                |                           |     |     |      |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> |     |     | 3    | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     |     |     | 100  | mV    |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        |     |     | 10   | %     | 1    |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    |     |     | 2    | dB    |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    |     |     | 2    | dB    |      |
| <b>Dynamic Range for Rated Accuracy</b>        |                           |     |     |      |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> | -20 |     | 85   | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     | 3.0 |     | 3.6  | V     |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        | 0   |     | 20   | mA    |      |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    | -9  |     | -2.5 | dBm   |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    | -20 |     | 0    | dBm   |      |
| <b>Max Reporting Range</b>                     |                           |     |     |      |       |      |
| Internally measured transceiver temperature    | DD <sub>Temperature</sub> | -40 |     | 125  | °C    |      |
| Internally measured transceiver supply voltage | DD <sub>Voltage</sub>     | 2.8 |     | 4.0  | V     |      |
| Measured TX bias current                       | DD <sub>Bias</sub>        | 0   |     | 20   | mA    |      |
| Measured TX output power                       | DD <sub>Tx-Power</sub>    | -10 |     | -3   | dBm   |      |
| Measured RX received average optical power     | DD <sub>Rx-Power</sub>    | -22 |     | 0    | dBm   |      |

Notes:

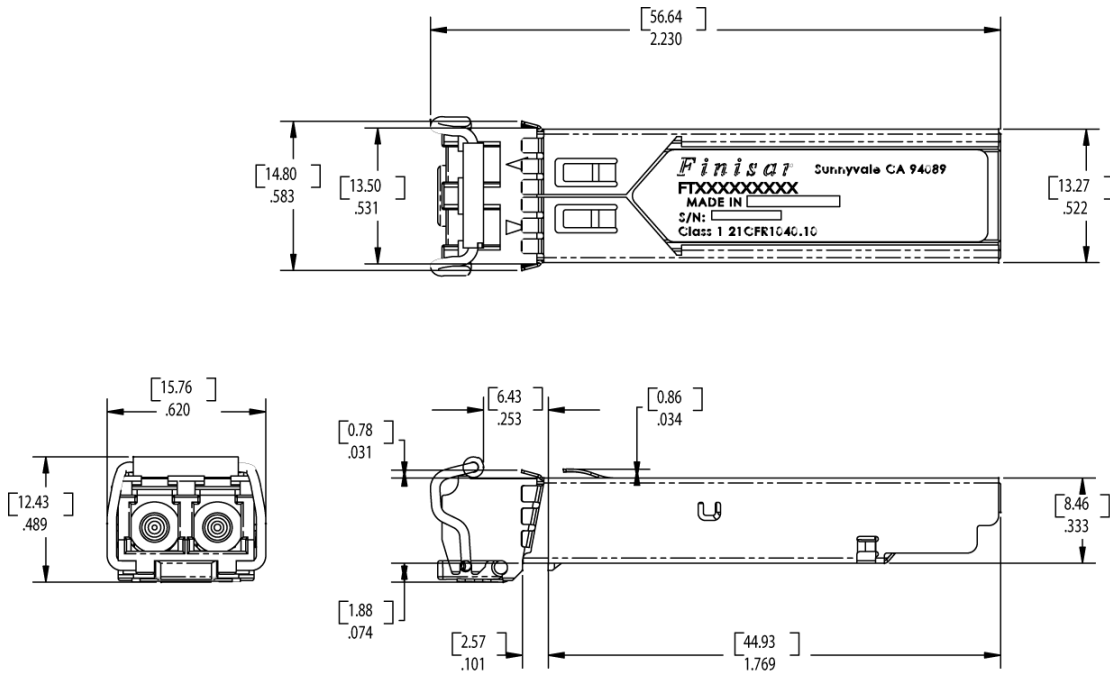
1. Accuracy of Measured Tx Bias Current is 10% of the actual Bias Current from the laser driver to the laser.

## X. Mechanical Specifications

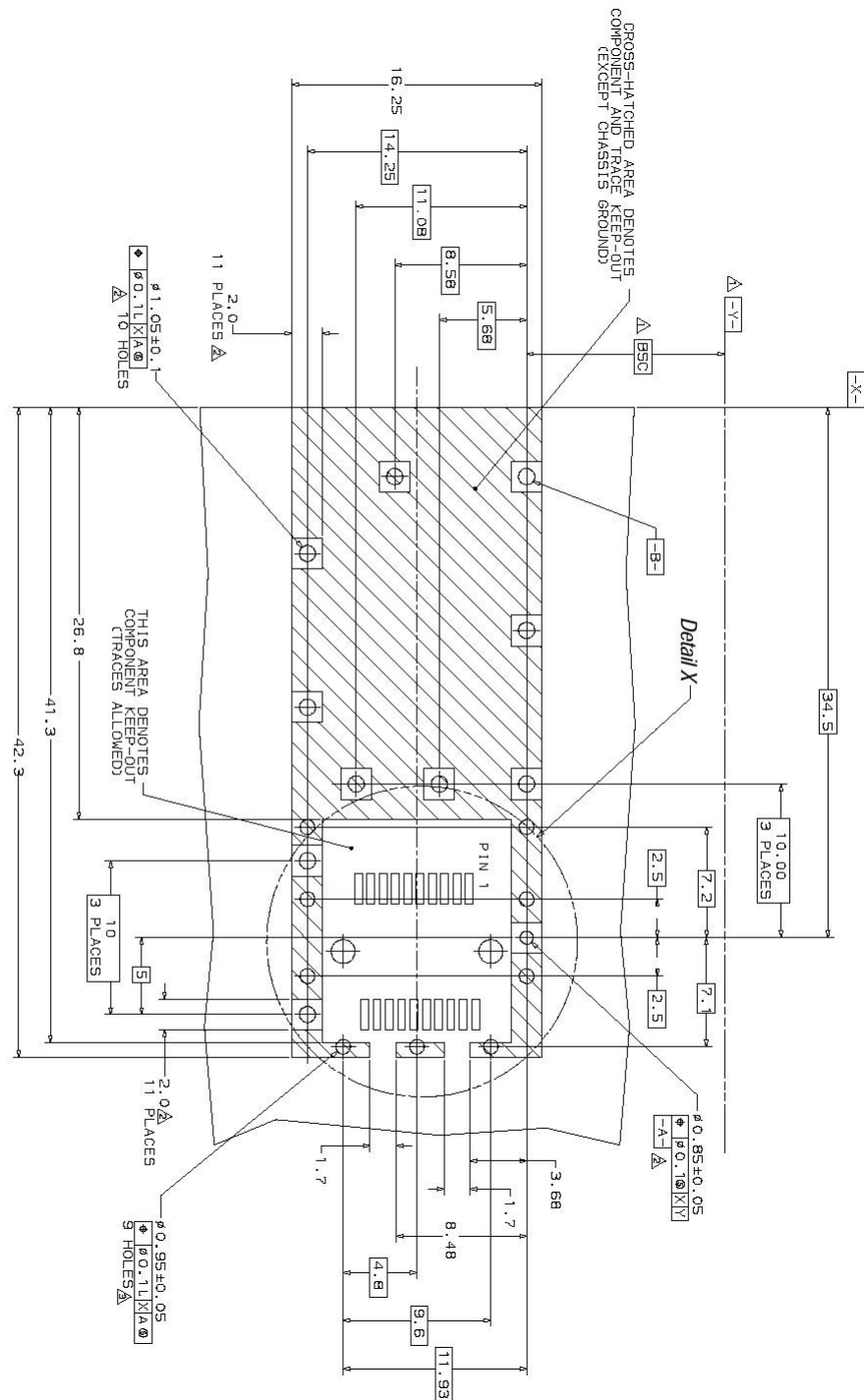
Finisar's Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).



**FTLF8524P2BNy**

**FTLF8524P2WNy**

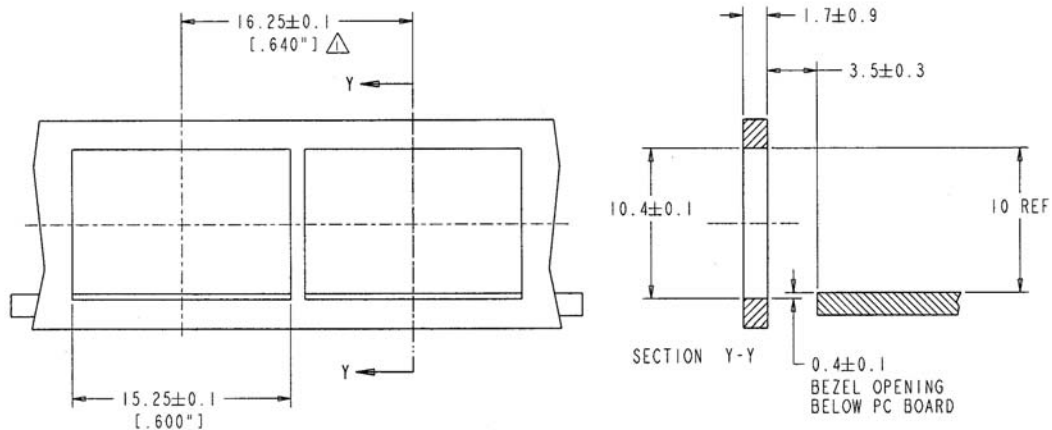
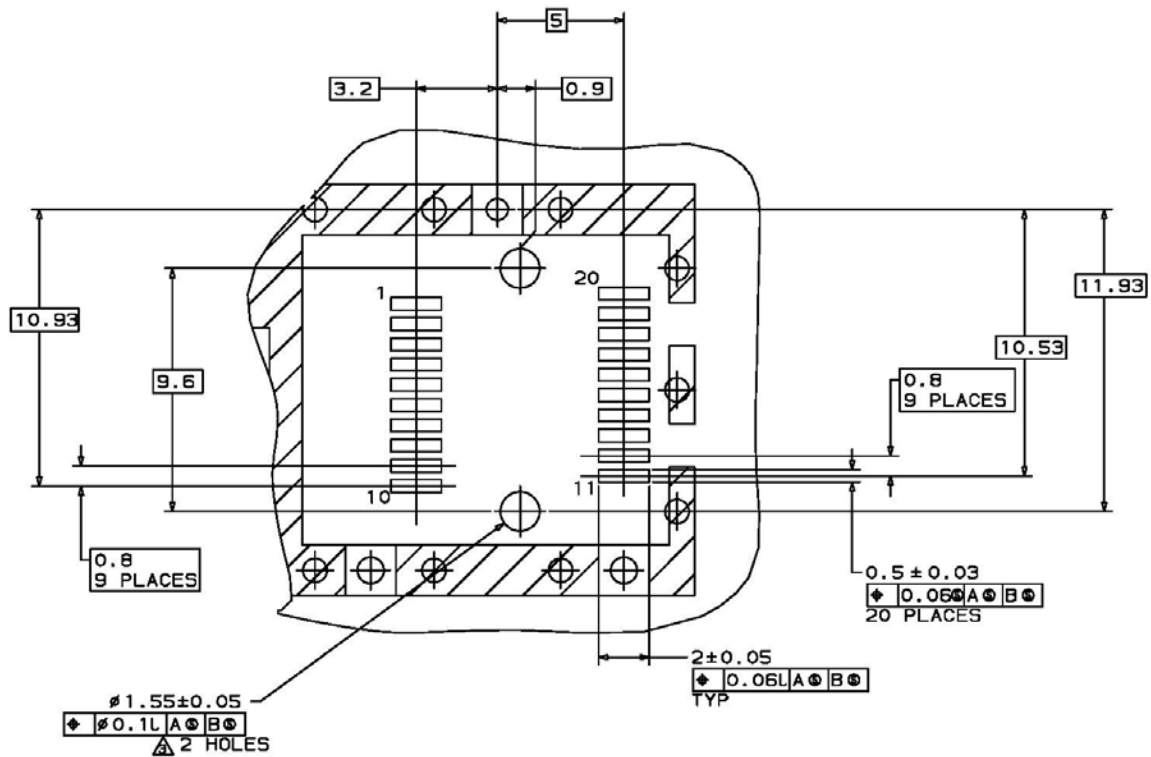
## XI. PCB Layout and Bezel Recommendations



### Datum and Basic Dimension Established by Customer

## 2 Rads and Vias are Chassis Ground, 11 Places

### ⚠ Through Holes are Unplated



NOTES:

2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

## **XII. References**

1. Small Form-factor Pluggable (SFP) Transceiver Multi-source Agreement (MSA), September 14, 2000.
2. “Fibre Channel Draft Physical Interface Specification (FC-P1-2 Rev. 7.0)”. American National Standard for Information Systems.
3. IEEE Std 802.3, 2002 Edition, Clause 38, PMD Type 1000BASE-SX. IEEE Standards Department, 2002.
4. Directive 2002/95/EC of the European Council Parliament and of the Council, “on the restriction of the use of certain hazardous substances in electrical and electronic equipment.” January 27, 2003.
5. “Digital Diagnostics Monitoring Interface for Optical Transceivers”. SFF Document Number SFF-8472, Revision 9.3.

## **XIII. For More Information**

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