MAINTENANCE MANUAL

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## PROPRIETARY NOTICE

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Within one year of purchase, Racal-Dana will repair or replace your instrument, at our option, if in any way it is defective in material or workmanship. All parts and labor charges will be paid by Racal-Dana Instruments. Just call Racal-Dana Product Service (collect) at (714) 833-1234 in U.S.A., Windsor (07535) 69811 in England or 027-7575 in France for assistance. We will advise the proper shipping address for your prepaid shipment. Your instrument will be returned to you freight prepaid.



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PUBLICATION DATE: JANUARY 1979

## FOR YOUR SAFETY

Before undertaking any maintenance procedure, whether it be a specific troubleshooting or maintenance procedure described herein or an exploratory procedure aimed at determining whether there has been a malfunction, read the applicable section of this manual and note carefully the WARNING and CAUTION notices contained therein.

The equipment described in this manual contains voltages hazardous to human life and safety and which is capable of inflicting personal injury. The cautionary and warning notes are included in this manual to alert operator and maintenance personnel to the electrical hazards and thus prevent personal injury and damage to equipment.

If this instrument is to be powered from the AC Mains through an autotransformer (such as a Variac or equivalent) ensure that the instrument common connector is connected to the ground (earth) connection of the power mains.

Before operating the unit ensure that the protective conductor (green wire) is connected to the ground (earth) protective conductor of the power outlet. Do not defeat the protective feature of the third protective conductor in the power cord by using a two conductor extension cord or a three-prong/two-prong adapter.

Maintenance and calibration procedures contained in this manual sometimes call for operation of the unit with power applied and protective covers removed. Read the procedures carefully and heed Warnings to avoid "live" circuit points to ensure your personal safety.

Before operating this instrument.

- 1. Ensure that the instrument is configured to operate on the voltage available at the power source. See Installation section.
- Ensure that the proper fuse is in place in the instrument for the power source on which the instrument is to be operated.
- Ensure that all other devices connected to or in proximity to this instrument are properly grounded or connected to the protective third-wire earth ground.

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Model 4002

## **SPECIFICATIONS**

#### DC VOLTAGE

RANGE	MAXIMUM READING	ACCURACY (12 months) 18°-28°C <u>+ (</u> % rdg + digits)	MAXIMUM ALLOWABLE INPUT
200mV	199.99	0.04% + 3d	1200V momentary
2 V	1,9999	0.04% + 1d	1200V momentary
20 V	19,999	0.04% + 1d	1200V
200 V	199,99	0.04% + 1d	1200V
1200 V	1200.0	0.04% + 1d	1200V

Temperature Coefficient (0°-18° and 28°-55°C):  $\pm (0.006\% + 0.2 \text{ digit})/^{\circ}\text{C}$  except  $\pm (0.006\% + 0.4 \text{ digit})/^{\circ}\text{C}$  on the 200mV range. Input Resistance:  $10\text{M}\Omega \pm 0.1\%$ 

Normal Mode Rejection Ratio:

Greater than 60dB at 50Hz and 60Hz. Common Mode Rejection Ratio (1kΩ unbalance):

Greater than 120dB at DC, 50Hz and 60Hz.

Settling Time: 1 second to within 1 digit of final reading.

#### **AC VOLTAGE**

	MAXIMUM	ACCURACY (12 months) (above 2000 counts) 18 <sup>0</sup> -28 <sup>0</sup> C; 100Hz-10kHz	0°-18° an	E COEFFICIENT d 28 <sup>0</sup> -55 <sup>0</sup> C · digits)/ <sup>0</sup> C
RANGE	READING	±(% rdg + digits)	45Hz-10kHz	10kHz-20kHz
200mV	199,99	0.7% + 15d	0.07% + 2d	0.15% + 3d
2 V	1,9999	0.6% + 15d	0.07% + 2d	0.15% + 3d
20 V	19.999	0.5% + 15d	0.05% + 2d	0.05% + 2d
200 V	199.99	0.5% + 15d	0.05% + 2d	0.05% + 2d
1000 V	1000.0	0.5% + 15d	0.05% + 2d	0.05% + 2d

Extended Frequency Accuracy:
-(45Hz-100Hz) ± (0.7% + 15 digits)
(10kHz-20kHz) ± (0.8% + 15 digits) on the 20V and higher ranges, ± (1.5% + 15 digits) on the 2V range, ± (2% + 15 digits)

on the 200mV range.
Response: True root mean square.

Crest Factor: 3.

Input Impedance:  $1M\Omega = 1\% \text{ shunted by less than } 75pF.$  Maximum Allowable Input Voltage:  $1000V \text{ rms}, 1400V \text{ peak}, 10^7V\text{-Hz maximum}$  Common Mode Rejection Ratio (1k $\Omega$  unbalance): 60dB at DC, 50Hz and 60Hz. Settling Time: 2.5 seconds to within 10 digits of final reading.

#### DC AND TRMS AC CURRENT

-	RANGE	MAXIMUM READING	18 <sup>0</sup>	Y (12 months) -28 <sup>0</sup> C g + digits)	MAXIMUM VOLTAGE BURDEN	SHUNT RESISTANCE
				C 45Hz-10kHz		
				love 2000 counts)		
	200 u A	199.99	0.2% + 2d	1% + 15d	0.2 V	1kΩ
	2mA	1.9999	0.2% + 2d	1% + 15d	0.2 V	100 Ω
	20mA	19,999	0.2% + 2d	1% + 15d	0.2 V	10 Ω
	200mA	199.99	0.2% + 2d	1% + 15d	0.25V	1 Ω
	2000mA	1999.9	0.2% + 2d	1% + 15d	0.6 V	0.1 Ω

 $\begin{array}{lll} \mbox{Maximum Input: } 2A, \ 250 \mbox{V DC or rms (fuse protected)}. \\ \mbox{Temperature Coefficient } & (0^0 - 18^0 \mbox{ and } 28^0 - 55^0 \mbox{C}); \\ \mbox{DC } \pm (0.01\% + 0.2 \mbox{ digits})/^0 \mbox{C}. \\ \mbox{AC } \pm (0.07\% + 2 \mbox{ digits})/^0 \mbox{C}. \\ \end{array}$ 

Crest Factor: 3

Analog Settling Time:

DC: 1 second to within 1 digit of final reading. AC: 2.5 seconds to within 10 digits of final reading.

#### RESISTANCE

RA	ANGE	MAXIMUM READING	18 <sup>0</sup> -	(12 months) -28°C + digits)	ACROSS L	VOLTAGE JNKNOWN ANGE	TEMPERATURE $0^{\circ}$ -18° and $\pm$ (% rdg + c	28°-55°C		IINAL CURRENT
			$HI\Omega$	LO Ω	нιΩ	LOΩ	ΗΙ Ω	LO Ω	<u>ΗΙ Ω</u>	<u>LO Ω</u>
	$2\mathrm{k}\Omega$	1,9999	-	0.15% + 15d		0.2V	_	0.02% + 2d	9 <u></u>	100µA
	20 kΩ	19.999	0.04% + 1d	0.15% + 15d	2V	0.2V	0.003% + 0.2d	0.02% + 2d	100µA	10µA
2	00 kΩ	199,99	0.04% + 1d	0.15% + 15d	2V	0.2V	0.003% + 0.2d	0.02% + 2d	10µA	1µA
20	00 kΩ	1999.9	0.04% + 1d	0.15% + 15d	2V	0.2V	0.003% + 0.2d	0.03% + 2d	1µA	0.1µA
	$20M\Omega$	19,999	0.10% + 1d	_	2V	_	0.02% + 0.2d		0.14A	-

Maximum Allowable Input: 450V rms sustained, 1kV DC or peak AC momentary. Maximum Open-Circuit Voltage: 5 volts.

Settling Time: 1 second to within 1 digit of final reading except 2 seconds on the 20M  $\!\Omega$  range.

#### GENERAL

DISPLAY: Five 0.5" LED digits, appropriate decimal position

and polarity indication.
CONVERSION PERIOD: 400 milliseconds.

ENVIRONMENT:
Operating: 0°C to 55°C;
0% to 80% relative humidity up to 40°C.

Storage: -25°C to +65°C.

POWER: 105-125 or 210-250 volts (switch selected), 90-110V available. 50-60Hz, 7 watts. Optional 6 hour battery pack, (Option 06).

DIMENSIONS, WEIGHT: 85mm high x 235mm wide x 275mm deep (3-1/2 in. x 9-1/4 in. x 10-3/4 in.).

Net weight: 1,7kg. (3 lbs., 13 oz).

OVERRANGE INDICATION: Display blinks all zeros above

19999 counts.
MAXIMUM COMMON MODE VOLTAGE: 1400V peak.

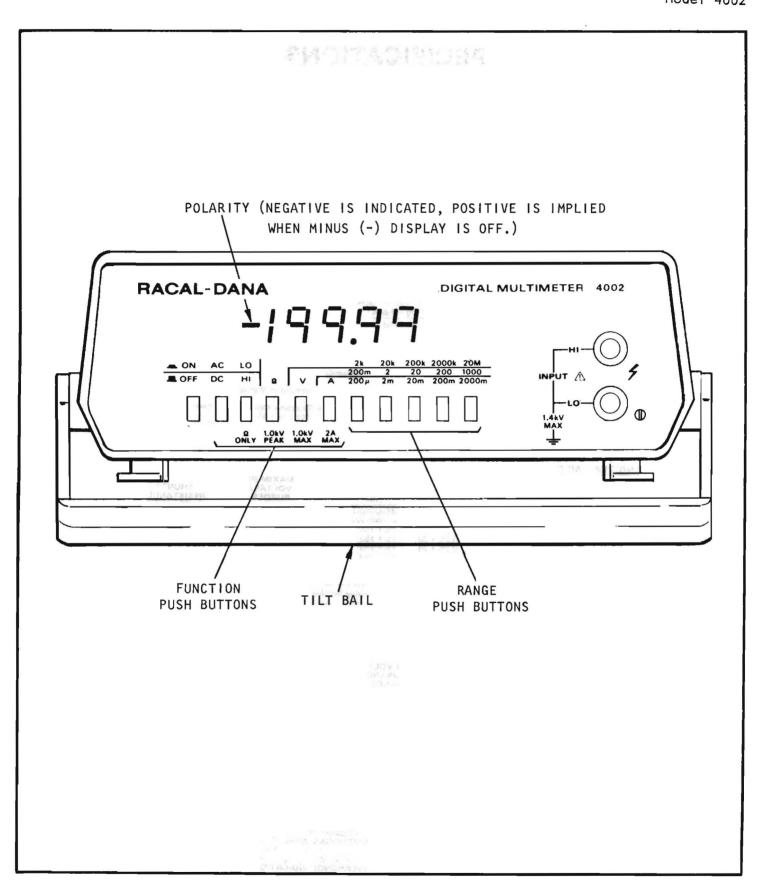


FIGURE 1-1. Front Panel.

Model 4002

#### SECTION 1. GENERAL INFORMATION.

- INTRODUCTION. The Model 4002 DMM is a versatile digital multimeter useful for measurement of ac and dc voltage, ac and dc current and resistance. Ranges and accuracies are listed in the Table of Specifications on Page v. Ranges and functions are selected with pushbuttons on the front of the unit. The 4002 automatically indicates polarity and positions the decimal point.
- WARRANTY INFORMATION. The Warranty is given on the back of the title page of this Instruction Manual. If there is a need to exercise the Warranty, contact the Racal-Dana Representative in your area to determine the proper action to be taken. Racal-Dana maintains service facilities in the United Kingdom and France, as well as in the United States. A listing of Representatives and Service Centers is included in the back of this manual.
- CHANGE NOTICES. Improvements or changes to the instrument which occur after printing of the Instruction Manual will be explained on a Change Notice sheet attached to the inside back cover.

#### IMPORTANT

The Zi symbol can be found in various places in this Instruction Manual. Carefully read the associated CAUTION statements with regard to proper use and handling of the instrument. Damage to the instrument may occur if these precautions are ignored.

symbol can be found in various places in this Instruction Manual. This symbol indicates those areas on the instrument which are potential shock hazards. Carefully read the associated WARNING statements with regard to proper use and handling of the instrument. Serious personal injury may result if these precautions are ignored.

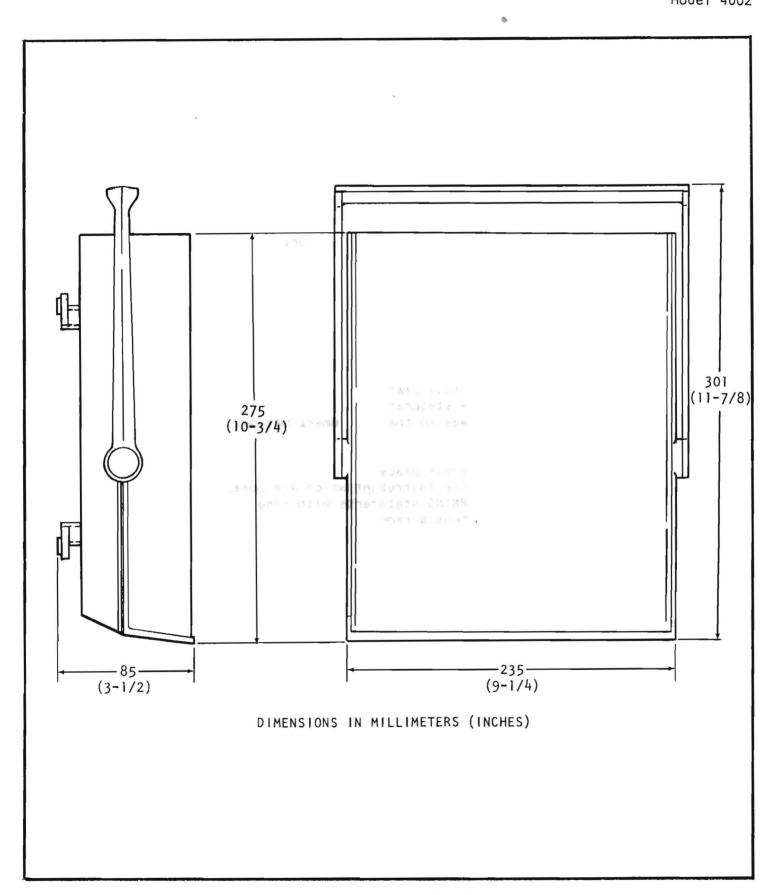


FIGURE 1-2. Dimensional Data

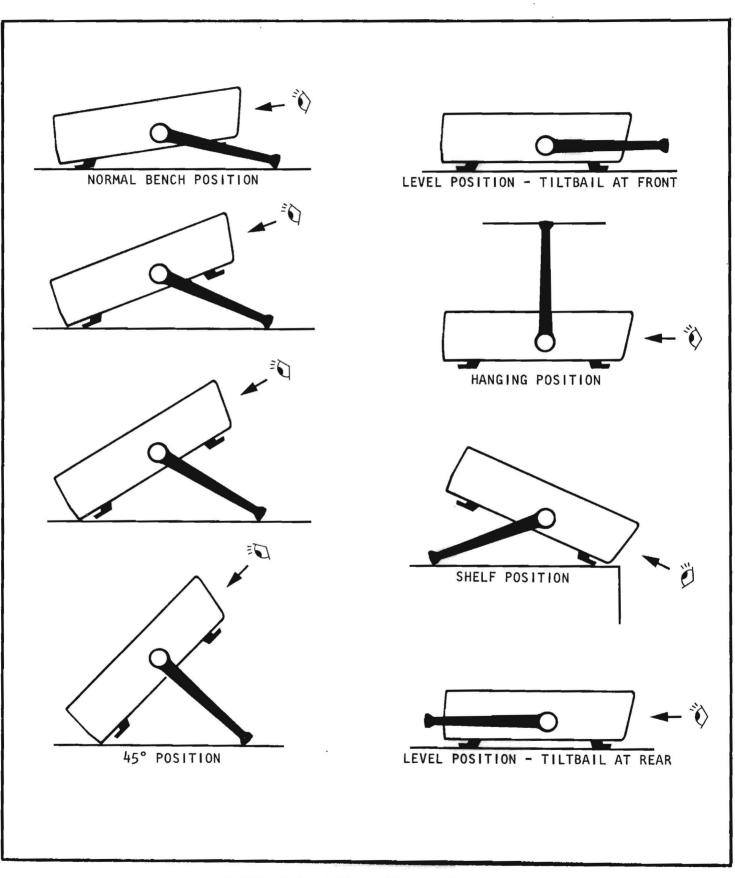


FIGURE 1-3. Tilt Bail Positions.

2-5. OPERATION ON BATTERY PACK POWER, NO TRANSPOLET, S' NOTTOSE May also be operated from rechargeable sealed lead-acid batteries contained in the Option 06 Battery Pack. The

- Circuits within the battery 2-1. GENERAL. This section provides information needed for incoming inspection and prepack will automatically to maintain accurate readings. Refer to Figure 2-2 and install the battery saucnoisansq
- 2-2. INSPECTION. The Model 4002 was carefully inspected both mechanically and electrically before shipment. Upon receiving the instrument, check for any obvious damage which may have occurred during transit. Report any damages to the shipping agent. To verify the electrical specifications, follow the procedures given in Section 3.
- PREPARATION FOR USE. The Model 4002 is shipped ready-to-use. The instrument may be powered from line voltage or from rechargeable batteries (when the Option 06 Rechargeable Lift off the callbration shield, and save it for later use. (beldatani alate year
- OPERATION ON LINE POWER. The Model 4002 DMM is provided with a three-wire line cord which mates with third-wire grounded receptacles. Connect the instrument to ac line power as follows: NOTE

- CAUTION

  Connect only to the line voltage selected. Application of incorrect voltage can damage the instrument.
- Set the LINE VOLTAGE switch on the back of the instrument to correspond to the line voltage available. Ranges are 105 to 125 volts and 210 to 250 volts ac as shown in e. Install the battery pack in the instrument so that it rests on the plasticing armini

#### he ground clip must make contact with the upper side of the battery pack plate WARNING

connector P1004 on the circuit board Ground the instrument through a properly grounded receptacle before operation. Failure to ground the instrument can result in severe injury or death in the event of short circuit or malfunction.

b. Plug the power cord into a properly grounded outlet. Operate the 4002 DMM as Make sure the connector is aligned so that all pins mate. 775, MOITD32 tni bediroseb

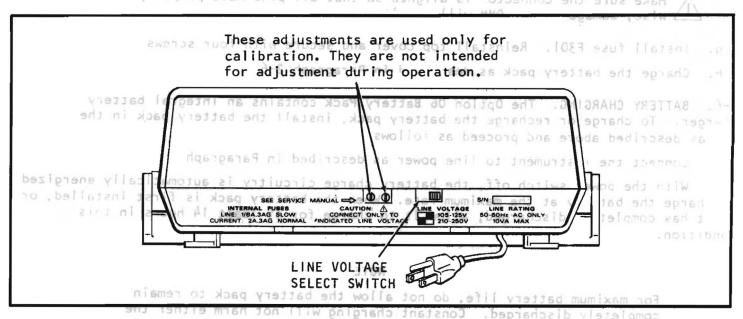


FIGURE 2-1. Rear View Showing Line Switch. To Assistance

#### **OPERATING INSTRUCTIONS**

2-5. OPERATION ON BATTERY PACK POWER. The Model 4002 DMM may also be operated from rechargeable sealed lead-acid batteries contained in the Option 06 Battery Pack. The battery pack will operate the 4002 DMM for up to 6 hours. Circuits within the battery pack will automatically shut down the instrument when the battery charge is insufficient to maintain accurate readings. Refer to Figure 2-2 and install the battery pack as follows: PROPERTION. The Model 4002 was carefully inspected both mechanically and

efore salpment. Upon receiving the instrument, check for any obvious to occurred Juring transit. Report any damages to the shipping ager Disconnect the line cord before removing the case cover.

- a. Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.
- b. Lift off the calibration shield, and save it for later use. The four plastic spacers must remain in place on the upright studs projecting through the main circuit The Model 4002 DMM is provided with a th servi board. which water in third-wire grounded receptacles. Connect the instrument to a

#### NOTE

Do not discard the calibration shield. This shield must be installed during calibration, as described in Section 4.

- c. Set the BAT/LINE switch to the BAT position shown in Figure 2-2. Note that the battery pack will not operate properly if this switch is not in the BAT position.
  - the. I the LINE VOLTAGE switch on the back of Remove fuse F301 on the battery pack. Joy 201 of 201 are segment and library packs.
- e. Install the battery pack in the instrument so that it rests on the plastic spacers. The ground clip must make contact with the upper side of the battery pack plate.
- f. Carefully align the battery pack plug with connector P1004 on the circuit board. Push the plug firmly onto the connector until the lip on the plug engages the lip on the connector to lock the plug in place.

## power cord into a properly WOLTUAD doublet. Operate the 4002 DAM

Make sure the connector is aligned so that all pins mate properly, other- $\angle ! \Delta$  wise, damage to the DMM will result.

- Install fuse F301. Reinstall top cover and secure with four screws.
- Charge the battery pack as described in Paragraph 2-6.
- BATTERY CHARGING. The Option 06 Battery Pack contains an integral battery charger. To charge or recharge the battery pack, install the battery pack in the DMM as described above and proceed as follows:
  - Connect the instrument to line power as described in Paragraph 2-4.
- With the power switch off, the battery charge circuitry is automatically energized to charge the battery at the maximum rate. When the battery pack is first installed, or if it has completely discharged, allow it to charge for at least 14 hours in this condition.

### NOTE VOLUME

For maximum battery life, do not allow the battery pack to remain completely discharged. Constant charging will not harm either the FIGURE 2-1. Read View Strument. World wall need 2-1.

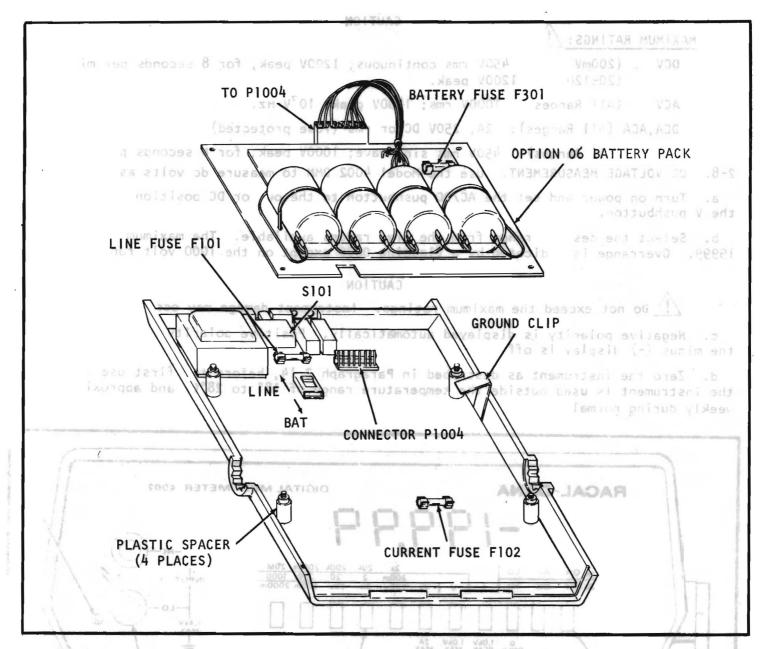


FIGURE 2-2. Battery Pack Installation

- c. When the 4002 DMM is in use on line power, the battery charger maintains a trickle charge on the battery pack.
- 2-7. OPERATING INSTRUCTIONS. Refer to Figure 2-3 and operate the DMM as follows:

The post of a carbonia halfs demosts a feed

a. Turn on the power by depressing the ON/OFF pushbutton.

JIAS TJIT

- b. Select the function with the AC/DC,  $\Omega$ , V, or A pushbuttons.
- c. Select the range by depressing the appropriate pushbutton. For resistance measurements only, also set the LO/HI pushbutton as desired.
- d. Connect the source to the INPUT terminals. Accessories described in Paragraph 2-15 should be used as required.

CAUTION

## MAXIMUM RATINGS: /!

DCV (200mV. 2V): 450V rms continuous; 1200V peak, for 8 seconds per minute.

(20-1200V): 1200V peak.

(All Ranges): 1000V rms; 1400V peak; 107V·Hz. ACV

DCA.ACA (All Ranges): 2A, 250V DC or rms (fuse protected).

(All Ranges): 450V rms sine wave; 1000V peak, for 8 seconds per minute. 2-8. DC VOLTAGE MEASUREMENT. Use the Model 4002 DMM to measure dc volts as follows:

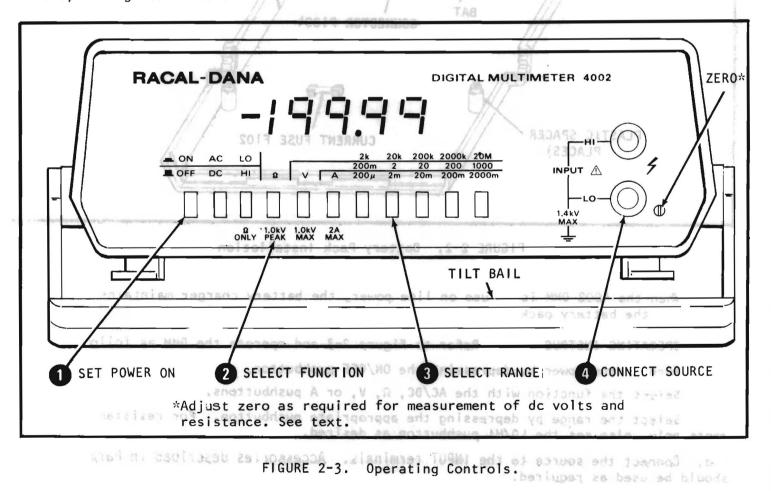
Turn on power and set the AC/DC pushbutton to the out or DC position. the V pushbutton.

Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000 except on the 1000 volt range.

#### CAUTION

/!ackslash Do not exceed the maximum ratings. Instrument damage may occur.

- c. Negative polarity is displayed automatically. Positive polarity is implied when the minus (-) display is off.
- d. Zero the instrument as described in Paragraph 2-14, before the first use whenever the instrument is used outside the temperature range of 18° to 28°C, and approximately weekly during normal use.



#### OPERATING INSTRUCTIONS

### INSTRUCTION MANUAL

Digital Multimeter

Model 4002

- 2-9. AC VOLTAGE MEASUREMENT. Use the Model 4002 DMM to measure ac volts as follows:
- a. Turn on power and set the AC/DC pushbutton to the in or AC position. Depress the V pushbutton.

## CAUTION

! Do not exceed the maximum ratings. Instrument damage may occur.

- b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000 except on the 1000 volt range. The instrument measures the true root mean square of a signal within the frequency range of 45 to 20k hertz. Maximum crest factor for rated accuracy is 3.
- c. Refer to Paragraph 2-13 for TRMS measurements of a signal with both ac and dc components.  $\sim$
- 2-10. RESISTANCE ( $\Omega$ ) MEASUREMENT. Use the 4002 DMM to measure resistance as follows:
  - a. Turn on power and depress the  $\Omega$  pushbutton.

#### CAUTION

! Do not exceed the maximum ratings. Instrument damage may occur.

- b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 00000. Use the LO/HI pushbutton as follows:
  - 1) Use the HI mode for measurements in the 20k, 200k, 2000k and 20M ohm ranges. Full range voltage drop is 2 volts and is sufficient to cause forward conduction of semi-conductor junctions. The HI terminal is positive.
  - 2) Use the LO mode for measurements in the 2k, 20k, 200k and 2000k ohm ranges. Full range voltage drop is 200 millivolts. Depressing 2k automatically selects LO mode; 20M selects HI mode. Maximum open circuit voltage is 5V on all ranges.
- c. Zero the instrument as described in Paragraph 2-14 before the first use whenever the instrument is used outside the temperature range of 18° to 28°C, and approximately weekly during normal use.
- 2-11. DC CURRENT MEASUREMENT. Use the 4002 DMM to measure dc current as follows:
- a. Turn on power and set the AC/DC pushbutton to the out or DC position. Depress the A pushbutton.

### CAUTION

Do notinstall a larger fuse than the one originally supplied (2A). Current fuse FlO2 protects the instrument against overcurrent.

- b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000. Overload is protected by fuse F102, a 2 amp 3AG normal-blow fuse.
- 2-12. AC CURRENT MEASUREMENT. Use the 4002 DMM to measure ac current as follows:
- a. Turn on power and set the AC/DC pushbutton to the in or AC position. Depress the A pushbutton.

#### **OPERATING INSTRUCTIONS**

#### CAUTION

Do not install a larger fuse than the one originally supplied. Current fuse F102 protects the instrument against overcurrent.

- b. Select the desired range from the five available ranges. The maximum reading is 19999. Overrange is indicated by a flashing 0000. The instrument measures the true root mean square of a waveform in the frequency range of 45 to 10k hertz. Overload is protected by fuse F102, a 2 amp 3AG fuse.
- 2-13. TRMS MEASUREMENT. The Model 4002 measures the ac component of a waveform and does not measure the dc component. For ac + dc measurements, use the procedure discussed in a. below.

#### NOTE

Accuracy is specified for 2000 counts and above. The method of calibrating the converter may yield an offset up to 50 digits with the input shorted. This does not affect the instrument accuracy.

- a. Use the 4002 DMM to measure TRMS on a signal which has both ac and dc components as follows:
  - 1. Turn on the power. Measure and record the ac and dc components separately.
  - 2. Compute the rms value from the following equation:

$$E_{RMS} = \sqrt{E_{DC}^2 + E_{AC}^2}$$

b. The crest factor (CF) is the ratio of the peak voltage to the rms voltage as follows:

$$CF = \frac{V_{PEAK}}{V_{RMS}}$$

1. Typical crest factors are as follows:

Sine wave 
$$CF = \sqrt{2}$$
  
Square wave  $CF = 1$   
Triangular wave  $CF = \sqrt{3}$   
Positive pulse train  $CF = 1/\sqrt{\text{duty cycle}}$   
(duty cycle for  $CF = 3$  is 0.11)

#### NOTE

There will be some additional measurement error for signals with a crest factor greater than 3 (CF>3).

- 2-14. ZERO ADJUSTMENT. The front panel zero adjustment nulls input offset on the 20, 200 and 1200 dc voltage ranges and on all resistance ranges. Typically, this adjustment need not be performed more often than once a week unless the instrument is operated at ambient temperatures outside the range of 18° to 28°C. Zero the instrument as follows:
  - a. Turn on the power and select LO  $\Omega$  and the 200k range.
- b. Plug in test leads and short them. Adjust the zero adjustment pot (R149) to obtain a reading of  $0000 \pm 3$  digits.

#### NOTE

The zero adjustment may also be used for lead compensation on a particular  $\Omega$  range.

meet the following conditions:

INSTRUCTION MANUAL **Digital Multimeter** Model 4002

instrument has been subjected to extremes of temperature, allow 3-1, GENERAL. Performance verification should be performed upon receipt of the instrument to ensure that no damage or misadjustment has occurred during transit. Verification may also be performed whenever there is question of the instrument's accuracy, and following calibration, if desired in Of not quantum of all wolls but MMC 2004 and no-man (2 be operated from either line power or from battery pack cower, as long as

#### cack has been fully charged as described igtOursgraph 2-5.

For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your Racal-Dana representative or the factory immediately.

- RECOMMENDED TEST EQUIPMENT. Recommended test equipment for performance verification is listed in Table 3-1. Alternate test equipment may be used. However, if the accuracy of the alternate test equipment is not at least 10 times better than the instrument specifications, additional allowance must be made in the readings obtained.
- ENVIRONMENTAL CONDITIONS. All measurements should be made at an ambient temperature within the range of 18° to 28°C (65° to 82°F), and a relative humidity of less than 80%.
- 3-4. PERFORMANCE VERIFICATION PRODECURE. Use the following procedures to verify the basic accuracy of the Model 4002 DMM for voltage, resistance and current measurements. If the instrument is out of specifications at any point, perform a complete calibration as described in Section 4, unless the instrument is still under warranty, as noted above.

TABLE 3-1. Recommended Test Equipment For Performance Verification.

5) Repeat all checks with negative voltage.

must be within the limits specified in Table 3-3.

ITEM	DESCRIPTION	SPECIFICATION	MFR.	MODEL
Α	DC Calibrator	0.1V, 1V, 10V, 100V, 1000V	Fluke	343A
В	AC Calibrator 70.00	±0.002% or 20µV 0.1V, 1V, 10V, 100V ±0.022%	200 mV 9-H	745A
С	AC Calibrator/Amplifier	1000V @ ±0.04% V 000.01	209¥H	745A/746A
D	Decade Resistor 20.00	1 040 1040 +0 019	200183	RS725
E	Current Source	100μA, 1mA, 10mA, ±0.006%	Fluke	3330B
F	Current Source	100mA, 1A, ±0.02%	Fluke	382A

Set the DMM to the 200 mV range and apply 100 mV ac to the DMM. The read is

4) Select the 2, 20 and 200 volt range 370Md apply the required voltages as sport Performance verification should be performed by qualified personnel using accurate and reliable test equipment.

901 37 1/19 1/13

and relieble test equipment.

- a. Initial Conditions. Before beginning the verification procedure the instrument must meet the following conditions:
  - 1) If the instrument has been subjected to extremes of temperature, allow internal temperatures to stabilize for one hour minimum at the environmental conditions specified in Paragraph 3-3.
  - 2) Turn-on the 4002 DMM and allow it to warm up for 10 minutes. The instrument may be operated from either line power or from battery pack power, as long as the battery pack has been fully charged as described in Paragraph 2-6.
    - 3) Zero the instrument as described in Paragraph 2-14.

#### in contact your Ascal-lana represeDNINARW or the Factory lomes.

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Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.

puents outside specificat

## b. DC Volts Checkout? applicate and of shem ad team sanswells is will be a recite

- Select dc voltage readings with the AC/DC and V pushbuttons.
- 2) Connect the DC Calibrator (Item A, Table 3-1) to the instrument.
- 3) Select the 200mV range, and apply positive 100 mVdc to the DMM. The reading must be within the limits specified in Table 3-2.
- 4) Select each remaining range and apply the required voltage as specified in Table 3-2, verify that the reading is within specifications.
  - 5) Repeat all checks with negative voltage.

TABLE 3-2.

DC Voltage Performance Check

TABLE 3-1.

Range	Applied Voltage	Allowable Readings at 18° to 28°C
1	V	(Brit: stor 0, IV, IV, IOV, 10V, 10V, 10V, 10V, 10V, 10V, 10V, 10
200 mV	100.00 mV	99.93 to 100.07
2 V	1.0000 V	0.9995 to 1.0005
20 V	10.000 V	\$40.0± 9.995 to 10.005   GunA   rossodita
200 V		191 ,0xe199.95 to 100.05
1200 V	1000.0 V	999.5 to 1000.5

#### c. AC Volts Checkout.

- Select ac voltage readings with the AC/DC and V pushbuttons.
- 2) Connect the AC Calibrator (Item B, Table 3-1) to the DMM. Set the calibrator frequency to 1 kHz.

100mA, 1A, ±0.02%

- 3) Set the DMM to the 200 mV range and apply 100 mV ac to the DMM. The reading must be within the limits specified in Table 3-3.
- 4) Select the 2, 20 and 200 volt ranges and apply the required voltages as specified in Table 3-3. Verify that the readings are within specifications.

Model 4002

5) To check the 1000 volt range, connect the AC Calibrator Amplifier (Item C, Table 3-1) to the output of the AC Calibrator per the manufacturer's instructions. Set it for an output of 1000 volts ac rms and verify that the DMM readings is within the the us current source (Item E or F, Table 3-1) to thestimil beijages

De within the limits in Table. E-E. BLAT - Seliget ni bel 1992 as AC Voltage Performance Check approximate 1994 se

Select the 200 pA range and apply a current of 100.00 cA to the CMM. The

Range	Applied Voltage	Allowable Readings at 18° to 28°C
200 mV	100.00 mV	99.15 to 100.85 mVn and of aller . vineras
2 V	1.0000 V	0.9925 to 1.0075 V
20 V	10.000 V	.2-E 3 9.935 to 10.065 V
200 V	100.00 V	99.35 to 100.65 V
1000 V	1000.0 V	Range V 2.8000 of 1.800 owasle Re

## d. Resistance Checkout. SS.001 of 88.88

- Select resistance readings by pressing the  $\Omega$  pushbutton.
- 2) Set the HI/LO pushbutton to HI and select the 20k  $\Omega$  range.
- Connect the decade resistor (Item D, Table 3-1) to the DMM.
- 4) Set the decade resistor to zero and measure the resistance of the test leads. Subtract this reading from the displayed reading in all of the following steps.
- Set the decade resistor to 19.000 k $\Omega$ . Verify that the reading is within the limits specified in Table 3-4.
- Select the next range and measure the next resistance as specified in Table 3-4. Verify that each reading is within specifications. Test each item in the table, switching the HI/LO pushbutton as indicated.

TABLE 3-4. Resistance Performance Check

HI/LO	Range	Resistance	Allowable Reading at 18° to 28°C
HI	20 kΩ	19.000 kΩ	18.990 to 19.010 kΩ
HI	<b>200</b> kΩ	190.00 kΩ	189.90 to 190.10 kΩ
, HI	<b>2000 k</b> Ω	1.9000 ΜΩ	1899.0 to 1901.0 kΩ
ні	<b>20</b> MΩ	19.000 MΩ	18.980 to 19.020 MΩ
LO	2 kΩ	1.9000 kΩ	1.8957 to 1.9043 kΩ
LO	20 kΩ	19.000 kΩ	18.957 to 19.043 kΩ
LO	200 kΩ	190.00 kΩ	189.57 to 190.43 kΩ
LO	2000 kΩ	1900.0 kΩ	1895.7 to 1904.3 kΩ

### e. DC Current Checkout.

- 1) Select dc current readings with the AC/DC and A pushbuttons.
- 2) Connect the dc current source (Item E or F, Table 3-1) to the DMM.
- 3) Select the 200  $\mu A$  range and apply a current of 100.00  $\mu A$  to the DMM. The reading must be within the limits in Table 3-5.
- 4) Select each range and apply the required current as specified in Table 3-5. Verify that the reading is within specifications.
- f. Analysis. If the instrument is out of specified limits at any point in Tables 3-2 through 3-5, calibrate the DMM as described in Section 4. If the unit is still under warranty, refer to the note in Paragraph 3-1.

DC Current Performance Check

Range	Applied Current	Allowable Reading at 18° to 28°C
200 uA	100.00 μΑ	99.88 to 100.22 µA
2 mA	1.0000 mA	0.9988 to 1.0022 mA
20 mA	10.000 mA	9.988 to 10.022 mA
200 mA	100.00 mA	99.88 to 100.22 mA
2000 mA	1000.0 mA	998.8 to 1002.2 mA

Day Est per to most united to

TABLE 3-4.
Resistance Performance Check

Neststance Performance Check			
Allowable Reading at 18° to .			
18.990 to 19.010 kg			
189,90 to 190,10 kg			
1899.0 to 1901.0 kg			
18.980 to 19.020 Mg			
1.8957 to 1.9043-KJ			
18.957 (0 19.01) 14			
189.57 to 190.43 km			
1997 - 1201 C. T. 2001			

#### SECTION 4. CALIBRATION

- 4-1. GENERAL. Calibration should be performed yearly (every 12 months) or whenever performance verification (See Section 3) indicates that the Model 4002 DMM is out of specifications. If any step in the calibration procedure cannot be performed properly, refer to Section 5 for Troubleshooting Information or contact your Racal-Dana representative or the factory.
- 4-2. RECOMMENDED TEST EQUIPMENT. Recommended test equipment for calibration is listed in Table 4-1. Alternate test equipment may be used. However, the accuracy of the alternate test equipment must be at least 10 times better than the instrument specification, or equal to Table 4-1 specifications.

TABLE 4-1.
Recommended Test Equipment For Calibration.

ltem	Description	Specification	Mfr.	Model
А	DC Calibrator	0.1V, 1V, 10V, 100V, 1000V ±0.002% or 20μV	Fluke	343A
В	AC Calibrator ( )	0.1V, 1V, 10V, 100V ±0.022%	H-P	745A
С	Decade Resistor	1.9KΩ, 190KΩ, ±0.01%	ESI	RS725

- 4-3. ENVIRONMENTAL CONDITIONS. Calibration should be performed under laboratory conditions having an ambient temperature of  $20^\circ$  to  $26^\circ$ C ( $68^\circ$  to  $78^\circ$ F), and a relative humidity of less than 80%.
- 4-4. CALIBRATION PROCEDURE. Perform the following adjustments to calibrate the 179 DMM and restore its operation to specified limits.
- a. Calibration Shield Installation. If the Option 06 Battery Pack is installed in the instrument it must be removed and the calibration shield reinstalled before calibration.

## WARNING

Disconnect the line cord before removing the case cover.

- 1) Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.
- 2) Push back the ground clip (shown in Figure 2-2) from the upper side of the battery pack and remove the battery pack from the spacers.
- 3) Calibration may be performed on battery power as long as the battery pack is sufficiently charged. Leave the battery pack plugged into the instrument, but set the battery pack behind the DMM on the bench or table.
- 4) Set the calibration shield in place on the spacers. The shield should read correctly when viewed from the front of the instrument.
- 5) Slide the ground clip over the top of the calibration shield so that it contacts the upper surface of the shield.

6) If battery power is not to be used, plug in the line cord.

#### b. Calibration Instructions.

#### WARNING

Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.

- 1) Refer to Table 4-2 and perform the listed adjustments in the sequence indicated. Note that the step sequence is also indicated on the calibration shield by boxed numerals. The sequence must be followed exactly because the adjustments are interrelated and dependent on the preceding steps.
- 2) If the indicated adjustment cannot be made to obtain the specified reading, refer to Section 2-5 for Troubleshooting Information.

TABLE 4-2.

#### Calibration Procedure

Step	Function	Range	Input	Adjustment Point	Desired Reading	Test Equipment*
1	DC V	2 V	+1.9 V	R107	1.9000	DC Calibrator (A)
2	DC V	200 mV	+190 mV	R108	190.00	DC Calibrator
3	DC V	2 V	+1.9 V	R107	1.9000	DC Calibrator
4	Ω LO	200 kΩ	Short	R149	Set Front Panel Zero to Mechanical Center.	None
5	Ω LO	200 kΩ	Short	R112	00.0±10 digits	None
6	Ω L0	200 kΩ	Short	R149	00.00 ±2 digits	None
7	Ω ΗΙ	200 kΩ	190 kΩ	R127	190.00	Decade Resistor(C)
8	. Ω LO	<b>2</b> kΩ	1.9 kΩ	R129	1.9000	Decade Resistor
9	DC V	200 V	+190 V	R103	190.00	DC Calibrator
10	DC V	20 V	+19 V	R126	19.000	DC Calibrator
11	DC V	1000 V	+1000 V	R128	1000.0	DC Calibrator
12	AC V	20 V	1 V at 1 kHz	R142	1.000	AC Calibrator (B)
13	AC V	20 V	10 V at 1 kHz	R143	10.000	AC Calibrator
14	AC V	20 V	l V at l kHz	R142	1.000	AC Calibrator
15	AC V	20 V	10 V at 1 kHz	R143	10.000	AC Calibrator
16	AC V	200 V	100 V at 10 kHz	C106	100.00	AC Calibrator
17	AC V	20 V	10 V at 10 kHz	C112	10.000	AC Calibrator
18	AC V	2 V	1 V at 10 kHz	+.0111	1.0000	AC Calibrator

#### SECTION 5. TROUBLESHOOTING.

5-1. GENERAL. The troubleshooting instructions contained in this section are intended for qualified personnel having a basic understanding of analog and digital electronic principles and components used in a precision electronic test instrument. Instructions have been written to assist in isolating the defective circuit or subcircuit. Isolation of the specific defective component has been left to the technician.

#### NOTE visuad

INE/BAT switch | Must be set to BAT for use with

For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your Keithley representative or the factory immediately.

- 5-2. TROUBLESHOOTING PROCEDURE. This section contains tables listing step-by-step checks of the major DMM circuits described in Section 6, Theory of Operation. Proceed as follows:
- a. In general, start troubleshooting with Table 5-1, Line Power Checks, to verify that the power supplies are providing the specified voltage to the electronic components.
- b. If trouble occurs on battery power only, or if battery operating time is substantially less than 6 hours after overnight charging, test the batteries and charging circuit per Table 5-2.
- c. Proper operation of the A/D converter & display should be verified before trouble-shooting the signal conditionings. Check these circuits per Tables 5-4 and 5-3, respectively.
- d. Problems with ac voltage ranges may involve the ac attenuator, the ac amplifier, or the ac converter. Check these circuits per Table 5-6 and 5-8.
- e. Check the dc voltage attenuator per Table 5-5 if problems occur with the dc voltage ranges. Check the resistance circuit per Table 5-7 if resistance measurements are erratic.

On main printed circuit board.

- f. If problems occur with current readings, check the current shunts and related circuits per Table 5-9.
  - g. All measurements are referenced to analog common (ground clip).

TABLE 5-1. Line Power Checks

Step	Item/Component	Required Condition	Remarks
1	S101 line switch	Must be set to 105-125V or 210- 250V as appropriate.	
2	S102 LINE/BAT switch	Must be set to BAT for use with battery pack.	
3	F101 line fuse	Continuity.	
4	P1007 line cord	Plugged into live receptacle.	
5	65164 Th-201	Turn on power.	
6	+5V pad*	+5 volts ±10%,	Output of VR104.
7		1147 volts Minimum. pathives an	Output of CR101, input to VR104.
8	+15V pad*	+15 volts ±10%.	Output of VR102.
9	ald be verified nexiqu	the ATR co.muminimistloy 7.71+ho	Output of CR102, input to VR102.
10		-15 volts ±10%.	Output of VR101.
11	is di w ausso sesido	-17.5 volts minimum18.5 volts minimum.	Output of CR102, input to VR101,
	Surrent shorts ar	ent readings, theck the	NOTE: Hot reg- ulator may indi- cate shorted load.

<sup>\*</sup> On main printed circuit board.

## **INSTRUCTION MANUAL**

Digital Multimeter

Model 4002

TABLE 5-2.
Battery Power Checks

	Battery Power Checks				
Step	Item/Component	Required Condition	Remarks		
1		Check AC line power per Table 5-1.			
2		Turn off power.			
3	S102 LINE/BAT switch	Move to BAT.			
4	P1007 line cord	Plugged into live receptacle.	Charge circuit checks.		
5	F301	Remove fuse and connect ammeter to fuse clip. O to 500mA charging rate, varies with line voltage and battery state of charge.			
5A	BT301 Batteries	Full charge is ~9.8 volts over 4 cells. R301 adjusts charging rate (float voltage).	If voltage is low and adjustment of R301 does not start charging, see steps 7 and following. If voltage is low and adjustment of R301 does start charging, see Table 5-10 for adjustment of battery charge voltage.		
6	Each battery cell voltage during charging.	Less than 3 volts for any cell.	High voltage or zero indicates damaged cell.		
7	Q301 anode	Full wave rectified A voltage, 15 VDC nominal.	Output of CRIO1.		
8	C304 +	+17.5 volts minimum.	Output of CR102. Triggers Q301 gate thru R306 and CR301 unless Q302 is on.		
9	Q302	Should saturate only when bat- tery approaches full charge.			
10	VR301	8.2V zener.			
11		Unplug line cord & turn power on.	Discharge checks.		
12	P1004 pin 8 or U301 pin 11	100 kHz 5V square wave.	Clock input. If no input, see step 12A.		
12A	VR104, IN	+7 volts minimum.	Battery voltage input to VR104.		
13	Q307 and Q308 base	Square wave, $\pm 0.7$ volts at 25 kHz.	Output of U301, ÷ 4.		
14	Q307, Q308 collector	Must oscillate from saturation to twice battery voltage (≃19 volts) at 25 kHz.	Inverter.		
15	C304, C305	±17.5 volts minimum (±25 volts typical with fully charged bat.).	Inverter Output, input to VRIO1 & VRIO2		

TABLE 5-3. Display

Step	Item/Component	Required Condition	Remarks
1		Turn on power. Any function or range except OHMS.	
2	+5V* or J1001, pin 5	+5 volts ±10%.	If low, check per Table 5-1.
3	U202, pins 2, 6, 7, 9 and 13	Digit drive LOW = Enabled.	LED cathode.
4	U201, pins 1, 2, 6 and 7	HI = Enabled.	BCD input to segment decoder/driver.
5	U201, pin 4	Positive-going signal lasting for 200 clock pulses.	Leading digit sup- pression. Output of U107A.
6	J1002, pin 9	Polarity line (SIGN) HI = off LO =	NOTE: Polarity output (in at J1002, pin 9) is inverted for VDC on 20 volt and higher ranges. Polarity output is disabled on AC and $\Omega$ .
7	J1001, pins 1, 2, 3 and 4.	Appropriate DP line high (on).	

with the car car car and

<sup>\*</sup> On main printed circuit board.

TABLE 5-4.

Step	Item/Component	Required Condition	Remarks
1	Referenc UIU6.	Turn on Power. Select 2 volt DC range and short inputs.	On this function and range (also on 200 mV DC), input
	integr / in	plov 1.0± 000:1+	HI connects thru R106, R136, and R135G to A/D, with-
	U103 i egrat	1.2 ±0.2 volts.	out attenuation.
2	Display	.0000 ±1 digit.	
3	TP10*	0.0000 volts.	A/D signal input.
4	Buffe*89T	+1.00 volt.	Reference output.
5	TP3*dila	And state of the s	Reference output.
6	TP4*	1121b 16.30±0.25bvolts.com	Reference zener voltage.
7	U106, pin 7	+l.00 volt.	Reference input to U106.
8	CLK*	0 to +5V square wave at 100 kHz.	Clock input.
9	TP6*	+1.0 ±0.1 volt.	Stored autozero
		tegrator waveform for not tour, full scale in	voltage.
10	U103, pin 11	+1.0 ±0.1 volt.	Σ-node voltage to integrator in UlO3
11	TP7*	-1.2 ±0.2 volt,	Ul03 integrator output voltage.
12	U104, pins 2, and 6	3, +1 volt.	Buffer voltage on U104.
13	External volta	Apply +1.9000 volts. Display must read 1.9000 ±1 digit.	Calibration point.
14	TP7*	Waveform per Figure 5-1.	Integrator output.
15	U103, pin 2 12	Waveform per Figure 5-1 during ramping of integrator output.	Comparator output.
16		Select 200 mV range and short inputs.	10
17	Display	00.00 ±3 digits.	Proceed if out-of- limits. Change selected value of R145 if tests 18-2 meet required conditions.
18	TP10*	0.0000 volts.	A/D signal input.

TABLE 5-4.
A/D Converter, continued

Step	Item/Component	Required Condition	Remarks
19	U106, pin 7	+0.100 volt.	Reference input to U106.
20	U103, pin 11	+1.000 ±0.1 volt.	$\Sigma$ -node voltage to integrator in UlO3.
21	TP7*	-1.2 ±0.2 volts.	Ul03 integrator output voltage.
22	U104, pins 2, 3	+100 millivolts.	Buffer voltage on U104.
23	U104, pin 6	+1 volt. They en.	Buffer voltage.
24	External voltage source	Apply +190 millivolts. Display must read 190.00 ±1 digit.	Calibration point.

<sup>\*</sup> On main printed circuit board.

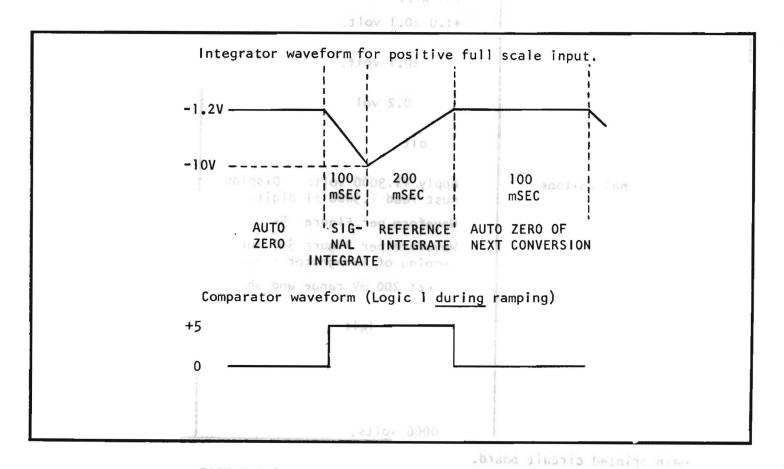


FIGURE 5-1. Integrator and Comparator Waveforms.

TABLE 5-5.

Step	I tem/Component	Required Condition	Remarks
1		Turn on power. Select 20 VDC range, and short inputs.	
2	R149	Front panel adjustment must zero the display.	
3	U101, pin 2	0.000 ±0.005 volts.	y .
4	External voltage source	Apply +10 volts from HI to LO.	Calibrated input.
5	TP5*	-1 volt	Output of U101.
6	External voltage source	(Apply +100 and +1000 volts on 200 and 1000 volt ranges.	Calibrated input.
7	TP5*	-1 volt	Output of U101 and feedback components, including relays.

DC Volts Attenuator

<sup>\*</sup> On main printed circuit board.

TABLE 5-6. AC Volts Attenuator and X10 Amplifier

Step	Item/Component	Required Condition	· Remarks
1		Turn on power. Select 2 VAC range.	NOTE: Full scale inputs should pro- duce ≃2 volts out- put at TP9.
2	External voltage source	Apply 1 volt rms at 1 kHz.	Calibrated input.
3	TP9* & TP5	1 volt rms.	Output of UlOl and feedback components.
4	External voltage source	10, 100 and 1000 volts rms on 20, 200 and 1000 volt ranges.	Calibrated input.
5	TP9* & TP5	l volt rms on all ranges except 200 millivolts.	Output of UlO2 and feedback components, including relays.
6	External voltage source	Apply 1, 10, 100V @ 20kHz on 2, 20 and 200V ranges respectively.	C106, C111, C112, C113, and C114.
7	External voltage source	Apply 10V @ 45Hz on 20V range.	c105, c115, c116.
8		Select 200 mV range.	
9	External voltage source	1.00 millivolts at 1 kHz.	Calibrated input.
10	TP5*	100 millivolts rms,	Output of UlOl and feedback components.
11	TP9*	I volt rms,	Output of UlO2.

<sup>\*</sup> On main printed circuit board.

Model 4002

TABLE 5-7. Resistance Circuit.

ition Remarks
. Select Ω, HI e. Short inputs.
input HI.
A/D input.
Reference voltage.
ge; must be +2 to
t. Calibrated resistanc
A/D input.
ge.
ts. Reference voltage.
ts. A/D input.
nges in similar  ded.  loading by the current setting resistor does not affect readout since
91

ul sour Select

<sup>\*</sup> On main printed circuit board.

TABLE 5-8. AC Converter

Step	Item/Component	Required Condition	Remarks
<sub>ĝ</sub> 1	H. pyts.	Turn on power. Select 20 VAC range.	
2	External voltage source	10 volts rms, 1 kHz.	Calibrated input.
3	TP9*	l volt rms (approximate).	Input to converter.
4	U105, pin 6 or TP10*	+1 volt DC.	Output of U105.
5	R143 01 S+	Gain adjustment must operate.	1 e, th
6	Repeat steps 4 & 5	10 volts rms, 45 Hz.	Low frequency response.
7	Repeat steps 4 & 5	10 volts rms, 20 kHz.	High frequency response.

<sup>\*</sup> On main printed circuit board.

TABLE 5-9.

Test other ranges in similar

+100 millivolts.

-100 millivolts.

(a) palie, (a)				
Step	Item/Component	Required Condition	Remarks	
1	F102	Continuity.	particular fragmany	
2	R123, R124, R137, R138, R139	Correct shunt value for specified range. See schematic.	Measure with an ohmmeter.	
3		Turn on power. Select DCA and 200 µA range.	٤	
4	External voltage source	0 to 3 volts.	Clamping must occur at ±2 volts.	

TABLE 5-10. Adjustment of Battery Charge Voltage

Step	Item/Component	Required Condition	Remarks
1		Instrument off,	
2	R301	Turn full CCW (maximum charge rate).	
3	BT301	Monitor battery voltage for > 9.8V.	Fully charged cells require several minutes to reach this level. Discharged cells require several hours.  CAUTION: charging to >10V for longer than 30 min. will reduce battery life.
4	R301	When cells reach 9.8V, turn DMM on and adjust to maintain 9.8V across BT301.	

VERALL OPERATION. THOUTAGE TO PROBHT . 6 MOITDBS or 200-millivolt First scale and conditions taken some scale and converter to handle full scale at and do voltage and current

6-1. GENERAL. This section contains circuit descriptions for the Model 4002 DMM and for the Option 06 Battery Pack. An overall block diagram of signal flow is provided in Figure 6-1. The overall schematic diagram, drawing 28992E, is contained in the back of this manual.

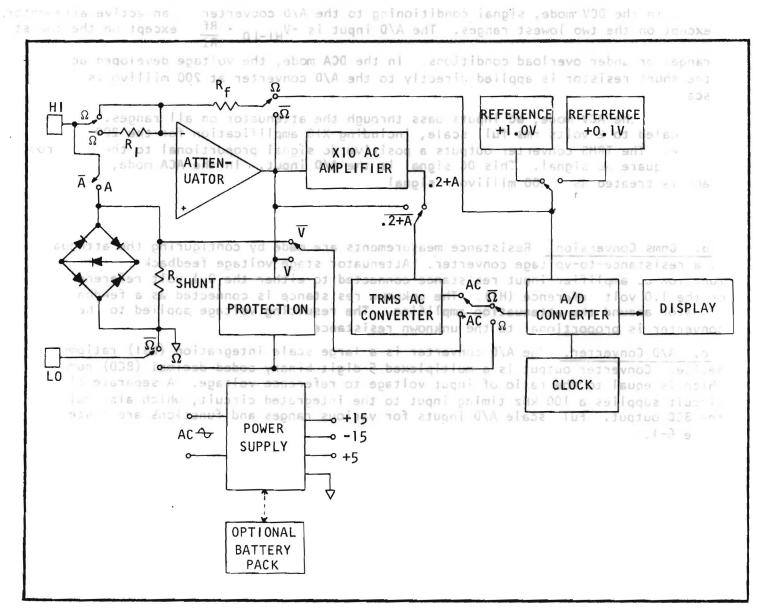


FIGURE 6-1. Simplified Signal Flow Block Diagram.

#### THEORY OF OPERATION

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- 6-2. OVERALL OPERATION. The Model 4002 DMM uses a 2-volt or 200-millivolt full scale analog-to-digital (A/D) converter with a 4-1/2 digit multiplexed display. Signal conditioning permits the A/D converter to handle full scale ac and dc voltage and current measurements over 5 decades, and to measure resistance over 5 ranges.
- a. Signal Conditioning. Signal conditioning includes dc attenuation (except on the 2 volt and 200 millivolt ranges), ac attenuation and X10 amplification, ac-to-dc conversion, ohms conversion and current shunts as shown in Figure 6-2.
  - l) In the DCV mode, signal conditioning to the A/D converter is an active attenuator, except on the two lowest ranges. The A/D input is  $-V_{HI-LO} \cdot \frac{Rf}{RI}$ , except on the lowest ranges or under overload conditions. In the DCA mode, the voltage developed accross the shunt resistor is applied directly to the A/D converter at 200 millivolts full scale.
  - 2) In the ACV mode, ac inputs pass through the attenuator on all ranges. The input is scaled to 2 volts rms full scale, including X10 amplification for the 200 millivolt range. The TRMS converter outputs a positive dc signal proportional to the true root mean square ac signal. This DC signal is the A/D input. In the ACA mode, shunt voltage is treated as a 200 millivolt signal.
- b. Ohms Conversion. Resistance measurements are made by configuring the attenuator as a resistance-to-voltage converter. Attenuator stage voltage feedback resistors Rf function as amplifier input resistance connected to either the 0.1 volt reference (LO) or the 1.0 volt reference (HI). The unknown resistance is connected as a feedback resistor around the attenuation amplifier. The resulting voltage applied to the A/D converter is proportional to the unknown resistance.
- c. A/D Converter. The A/D converter is a large scale integration (LSI) ratiometric device. Converter output is a multiplexed 5 digit binary coded decimal (BCD) number which is equal to the ratio of input voltage to reference voltage. A separate clock circuit supplies a 100 kHz timing input to the integrated circuit, which also multiplexes the BCD output. Full scale A/D inputs for various ranges and functions are listed in Table 6-1.

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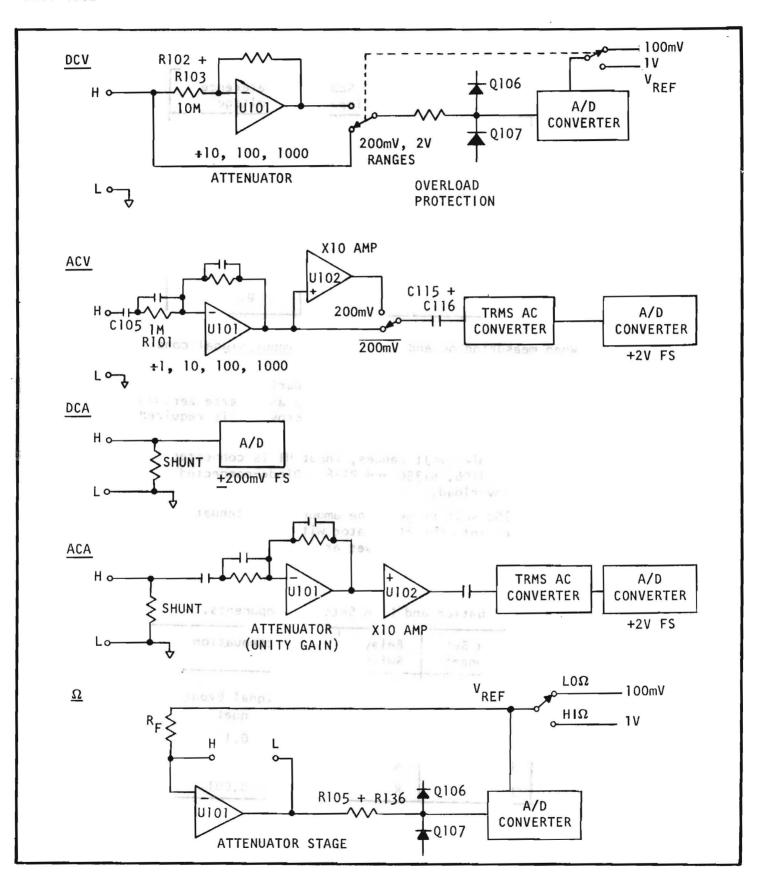


FIGURE 6-2. Attenuation and Ohms Conversion.

TABLE 6-1.
Full Scale A/D Inputs.

Function	Range	Full Scale A/D Input	Reference Voltage
DCV	200 mV	200 mV	0.1 V
DCV	2, 20, 200 1200 V	2 V	1.0 V
ACV	All	2 V	1.0 V
DCA	All	200 mV	0.1 V
ACA	A11	2 V	1.0 V
Ω	ні	2 ·V	1.0 V
	LO	200 mV	0.1 V

- 6-3. ATTENUATION. When measuring ac and dc voltages, input signal conditioning is provided by inverting amplifier U101 and additional components as described below.
- <u>a. DC.</u> Input resistance is set by R102 and R103. During calibration, R103 is adjusted to obtain a total input resistance of 10 M $\Omega$ . Both fine and coarse zero adjustments are provided since an amplifier output resolution of 10 microvolts is required for L0 resistance measurements.
  - 1) On the 2 volt and 200 millivolt ranges, input HI is connected to the A/D converter through protection resistors R106, R135G and R136. Diode-connected FET's Q106 and Q107 clamp the A/D input during overload.
  - 2) On the 20, 200 and 1200 volt ranges, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays K101, K102 and K103. Gain setting components and attenuation values are listed in Table 6-2.

TABLE 6-2.

DC Attenuation and Gain Setting Components.

Range	Gain Set Components	Relay/ Switch	Attenuation
200 mV 2 V			Signal bypasses attenuator
20 V	R118, R126	K101	0.1
200 V	R119, R127	K102	0.01
1200 V	R120, R128	К103	0.001

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<u>b. AC Volts.</u> Input resistance is  $IM\Omega$  (R101). Shunt capacitance is typically less than 75 pF. Additional conditioning is as follows.

1) For all ranges except the 200 millivolt range, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays K101 through K104. For the 200 millivolt range, non-inverting X10 amplifier U102 boosts the signal to 2 volts full scale. Gain setting components and attenuation values are listed in Table 6-3.

TABLE 6-3.

ALC AC Attenuation Gain Setting Components

1				
Range	Gain Set Components	Relay Energized	Attenuation	Freq. Comp. Capacitors
200 mV	R118, R126	K101.6	1 (X10*)	c106, c111
2 V	R118, R126	K101		c106, c111
20 V	R119, R127	K102	0.1	C106, C112
200 V	R120, R128	YS K103	istance [6.0 ureme	c106, c113
1000 V	R121, R122,	s c401X te	from gr100.0 and	c106, c114
$\infty$ ) then $\omega_0$	own resistes (1	The unkn	described below	tion components

<sup>\*</sup> Signal applied to X10 ac amplifier U102.

2) On the 200 millivolt and 2 volt ranges, high frequency compensation is adjusted with capacitor Clll, as shown in Table 6-3. On the 20 volt range, adjustment is performed with Cll2. On the 200 and 1000 volt ranges, adjustment is performed with Cl06. Some low frequency rolloff is introduced by input blocking capacitor Cl05, and ac converter input capacitors Cl15 and Cl16.

was the voltage across  $R_{\rm X}$  is either 2 volts (HI) or 200 millivolts (LO). Rufe

- 6-4. AC CONVERSION. The ac converter is a monolithic TRMS module. Output  $V_{dc} = \sqrt{Avg(Vin)^2}$  Potentiometer R143 provides gain adjustment, and R142 establishes output zero. Settling time and ripple are determined by C110 and C120. Low frequency rolloff is a function of C120.
- 6-5. OHMS CONVERSION. During calibration, the 10 M $\Omega$  input resistance (R102 and R103) and all attenuator feedback resistors are adjusted for both ratio and absolute value. Therefore, these resistors can also serve as reference (current setting) for resistance measurements. In the  $\Omega$  mode, the attenuation (feedback) resistors are disconnected from the output of the attenuation amplifier (U101) and are connected instead to the A/D converter reference voltage. Since two reference voltages and two A/D converter gains are available, the Model 4002 DMM provides the option of measuring resistance with the sense current reduced by a factor of 10.
- a. Range Selection. Operation of the range pushbuttons selects range resistors to provide the reference current listed in Table 6-4. Operation of the HI/LO pushbutton selects the 1 volt or 0.1 volt reference, respectively. Relay K105 is always energized in the  $\Omega$  mode.

iz, particularly the 50 and 60 hertz line frequencies.

### TABLE 6-4, Resistance Range Setting Components

Range	Range Resistors	Relay/Switch	Nom. I REF in HI $\Omega$	Nom. I REF in LO Ω
2 kΩ	R121, R122, R129	K104	<u>-</u>	100μΑ
20 kΩ	R120, R128	K103	100µА	10μΑ
200 kΩ	R119, R127 (2009mo)	K102	10μΑ	1μΑ
2000 kΩ	R118, R126	Reley 101X ALL	lμA	0.1μΑ
20 MΩ	R102, R103	1000 switch,	0.1μΑ	-
	eut (#0)	pins 17, and Ω 8, 9.		

- b.  $\Omega$  Circuit. For resistance measurements, relay K105 and terminals 4, 5, and 6 of the  $\Omega$  pushbutton connect the input HI terminal directly to the amplifier summing node. Input L0 is disconnected from ground and is connected to the A/D converter input through the protection components described below. The unknown resistance (R<sub>X</sub>) then becomes the amplifier feedback resistance.
  - l) Current flow in the unknown resistance is from input HI to input LO. At full scale, the voltage across  $R_X$  is either 2 volts (HI) or 200 millivolts (LO). Reference source loading does not affect accuracy since the A/D converter is ratiometric.
  - 2) The HI terminal is clamped to analog common by Q101 and Q102. The instrument protection network at the amplifier output consists of a pulldown resistance (R104 and CR103, CR104 and CR105). R104 sinks approximately 150 microamps. During in-range measurements, this current is supplied by the reference voltage through CR105 and voltage through the amplifier (U101) and CR104. Overloads with input HI positive are sustained by CR105; diodes CR103 and CR104 sustain negative overloads. Open circuit voltage is set to less than 5V by R150 and R151 through CR103 and CR105. A/D protection in  $\Omega$  is the same as in V except R105 is substituted for R106.
- 6-6. A/D CONVERTER. The A/D converter operates on the dual slope principle. The timing is divided into three periods as described below. Operation with high and low reference voltages is described separately in subparagraph d.
- a. Auto-Zero. The auto-zero period (A, Figure 6-3) is 100 milliseconds in length, which corresponds to 10,000 clock pulses. During this period, reference voltage  $V_{REF}$  (see subparagraph d) is stored on capacitor C124. Capacitor C117 stores  $V_{REF} + V_{OS1} V_{OS2}$ .
- b. Signal-Integrate. The signal-integrate period (B, Figure 6-3) is 100 milliseconds in length. The A/D input is buffered by U104 (see subparagraph d) and integrated by U103. Positive signals generate a negative-going ramp at the integrator output (pin 14), while negative signals produce a positive-going ramp. The level of the integrated signal at the end of the signal-integrate period is proportional to the average of the applied signal during this period. Since signal integration continues for 100 milliseconds, the A/D converter exhibits high normal mode rejection for ac signals in multiples of 10 hertz, particularly the 50 and 60 hertz line frequencies.

### THEORY OF OPERATION

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INSTRUCTION MANUAL Digital Multimeter Model 4002

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- c. Reference-Integrate. The reference-integrate period (C or D, Figure 6-3) is 200 milliseconds or 20,000 counts in length. During this period, the integrator is returned to baseline level by applying a reference voltage of a polarity opposite to that of the signal. A positive-going ramp is obtained by grounding the buffer input, while a negative going ramp is produced by the integration of 2 x  $V_{\rm REF}$  (that is,  $V_{\rm REF}$  + the voltage stored on C124). The time, or number of clock pulses required for discharge is proportional to the signal input. Digital output is from latches within U106 which store the number of clock pulses required for the integrator to return to baseline level. The maximum count during this period is 20,000 which corresponds to a discharge period of 200 milliseconds or full scale input.
- <u>d. Reference Voltages.</u> Reference voltage V<sub>REF</sub> may be either 1 volt or 0.1 volt. Switching through the pushbuttons turns on either Q104 (for 1 volt) or Q103 (for 0.1 volt). The voltages are provided by a divider across a temperature compensated zener diode. An operational amplifier on U103 provides the zener with a self-regulating bias. Use of the 0.1 volt reference increases converter sensitivity to 200 millivolts full scale, permitting accurate L0 ohms operation, 10 microvolt resolution on dc voltage measurements, and dc amperage measurements with a full scale burden of 200 millivolts. Increased sensitivity is accomplished by switching input buffer U104 into a gain-of-10 configuration by turning on Q105. Auto-zero charging on C124 is to a 100 millivolt reference instead of a 1 volt reference. Integrator and comparator voltage levels are unaffected by buffer gain. Buffer offset voltage is zeroed, and resistors R146 plus R144 or R145, which are selected at test, null any remaining zero offset on the 200 millivolt range.
- 6-7. DISPLAY. Five light-emitting diodes (LED) are driven by U201, which is a CMOS BCD-to-seven segment decoder/driver with bipolar current-sourcing outputs. Segment currents are limited to approximately 20 milliamperes peak by resistor network R202. The LED readout is a multiplexed, common-cathode configuration with Darlington array U202 sequentially sinking current from each digit. Blanking of the overrange digit is accomplished by gates U107A and U107B. Emitter-follower Q108 ensures that CMOS-compatible levels are maintained on U107A, pin 1, regardless of the loading of U202. The minus polarity readout is blanked on ac voltage and resistance ranges by contacts on the pushbutton switch. Proper decimal point position is determined by the combination of function and range selected.
- 6-8. CURRENT MEASUREMENTS. In the A mode, the signal is switched into one of five current shunts ahead of the attenuator section. For dc current measurements, the shunt voltage drop is applied directly to the A/D converter input at 200 millivolts full scale. For ac current measurements, the shunt voltage drop is treated as a 200 millivolt ac signal and passes through the ac attenuator and the XIO ac amplifier. Overload clamping occurs at three diode drops which is a level high enough to permit high crest factor current waveforms.
- 6-9. AC POWER SUPPLY. When the DMM is operated from ac line power, the power supply furnishes +5, +15, and -15 volts from regulators VR104, VR102 and VR101, respectively. Full-wave rectified ac from bridge rectifiers CR101 and CR102 is filtered by reservoir capacitors C108, C104 and C103 and is applied to the linear voltage regulators.

FIGURE 6 -- AID CONVET

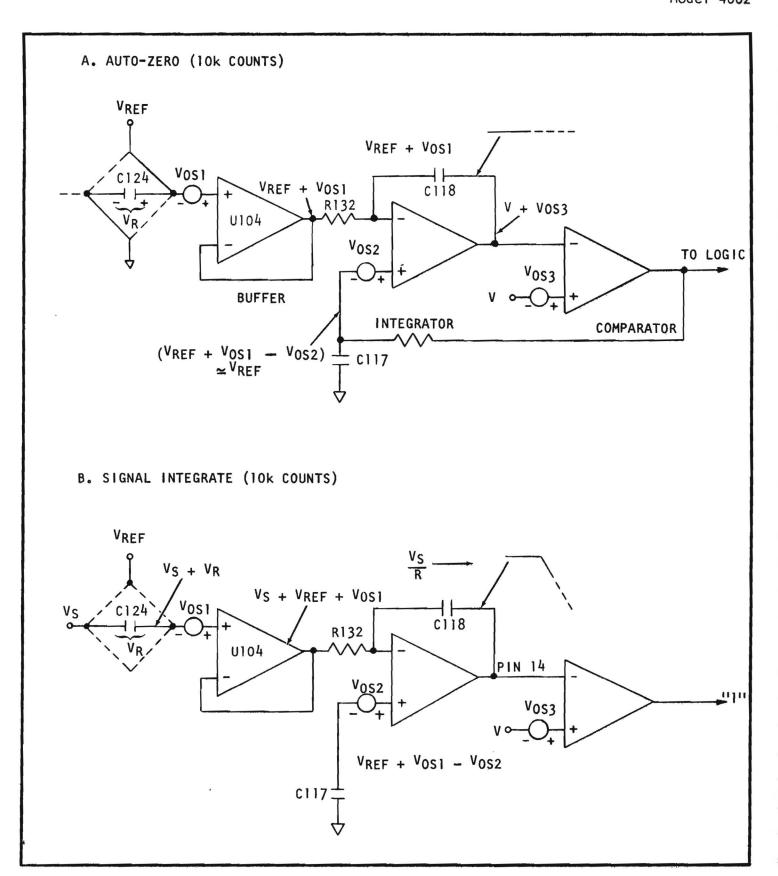


FIGURE 6-3. A/D Converter Function (Sheet 1 of 2)

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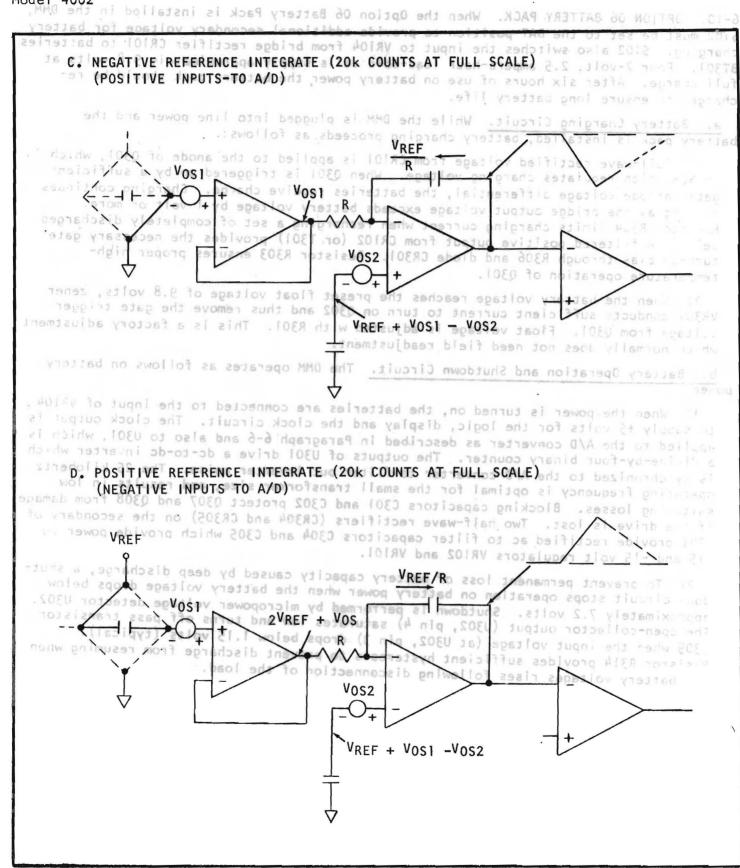


FIGURE 6-3. A/D Converter Function (Sheet 2 of 2)

### THEORY OF OPERATION

- 6-10. OPTION 06 BATTERY PACK. When the Option 06 Battery Pack is installed in the DMM, \$102 must be set to the BAT position to provide additional secondary voltage for battery charging. \$102 also switches the input to VR104 from bridge rectifier CR101 to batteries BT301. Four 2-volt, 2.5 ampere-hour lead-acid cells supply approximately 9.8 volts at full charge. After six hours of use on battery power, the battery pack should be recharged to ensure long battery life.
- a. Battery Charging Circuit. While the DMM is plugged into line power and the battery pack is installed, battery charging proceeds as follows:
  - 1) Full-wave rectified voltage from CR101 is applied to the anode of Q301, which is an SCR which regulates charging voltage. When Q301 is triggered on by a sufficient gate-cathode voltage differential, the batteries receive charge. Charging continues as long as the bridge output voltage exceeds battery voltage by 1 volt or more. Resistor R304 limits charging current when recharging a set of completely discharged cells. A filtered positive output from CR102 (or T301) provides the necessary gate turn-on bias through R306 and diode CR301. Resistor R303 ensures proper high-temperature operation of Q301.
  - 2) When the battery voltage reaches the preset float voltage of 9.8 volts, zener VR301 conducts sufficient current to turn on Q302 and thus remove the gate trigger voltage from Q301. Float voltage is adjusted with R301. This is a factory adjustment which normally does not need field readjustment.
- b. Battery Operation and Shutdown Circuit. The DMM operates as follows on battery power:
  - 1) When the power is turned on, the batteries are connected to the input of VR104 to supply +5 volts for the logic, display and the clock circuit. The clock output is applied to the A/D converter as described in Paragraph 6-6 and also to U301, which is a divide-by-four binary counter. The outputs of U301 drive a dc-to-dc inverter which is synchronized to the A/D converter to filter out inverter noise. The 25 kilohertz operating frequency is optimal for the small transformer size, and results in low switching losses. Blocking capacitors C301 and C302 protect Q307 and Q308 from damage if the drive is lost. Two half-wave rectifiers (CR304 and CR305) on the secondary of T301 provide rectified ac to filter capacitors C304 and C305 which provide power to +15 and -15 volt regulators VR102 and VR101.
  - 2) To prevent permanent loss of battery capacity caused by deep discharge, a shutdown circuit stops operation on battery power when the battery voltage drops below approximately 7.2 volts. Shutdown is performed by micropower voltage detector U302. The open-collector output (U302, pin 4) saturates low and turns off pass transistor Q309 when the input voltage (at U302, pin 3) drops below 1.15 volts (typical). Resistor R314 provides sufficient hysteresis to prevent discharge from resuming when the battery voltages rises following disconnection of the load.

VREF + V051 -V052

#### SECTION 7. REPLACEABLE PARTS

- 7-1. GENERAL. This section contains information for ordering replacement parts. HThe parts list is arranged in alphabetical order of their Circuit Designations.
- 7-2. ORDERING INFORMATION. To place an order or to obtain information concerning replacement parts, contact Racal-Dana Instruments, Irvine, California 92713, (714) 833-1234. When ordering, include the following information:

Model 4002 4-1/2 Digit TRMS Multimeter (Page 7-11)

Option Ob Rechargeable Battery Pack (Page 7-13).

- a. Instrument Model Number.
- b. Instrument Serial Number.
- c. Part Description.
- d. Circuit Designation (if applicable).
- e. Part Number.
- 7-3. MAINTENANCE KIT. A Maintenance Kit is available that contains a complement of spare parts that will maintain up to ten Model 4002 DMMs. Specify Part Number 29197A when ordering.

TABLE 7-1.
Model 4002 Maintenance Kit.

Qty.	Part Number	Schematic Callout
1 2 1 5 2 1 1 1 1 1 1 1 1 1 1 1	C-294-4 DD-16 DD-17 FU-13 FU-20 1C-93 1C-102 1C-165 1C-168 1C-169 1C-170 1C-174 LSI-11	C124 DS202, 203, 204, 205 DS201 F102 F101 VR104 V107 U101, 102 U201 U202 VR102 VR101 U106 U103
2	RL-59 TG-128	K101, 102, 103, 104, 105 Q101, 102, 106, 107

7-4. SCHEMATIC.

- a. Model 4002 4-1/2 Digit TRMS Multimeter: Schematic No. 28992E (Page 7-10). This schematic also describes the Option 06 Rechargeable Battery Pack.
- 7-5. COMPONENT LAYOUT.
  - a. Model 4002 4-1/2 Digit TRMS Multimeter (Page 7-11).
  - b. Option 06 Rechargeable Battery Pack (Page 7-13).

is available that cortains a co

Model Aput Maintenance Ki

ic Callout	Schene t	raidmyr' rhs3.
	c12k	A 785-5
203: 204: 205	05202,	A* - m/
	DS20	
	102	
	F101.	
	VR104	88
	701V	162
	1010	
		691-
	FOTEV	
		11-1
		21-12
3, 103,		-2-

## INSTRUCTION MANUAL Digital Multimeter

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Model 4001 Model 4002

## TABLE 7-1. Cross-Reference of Manufacturers

MFR.	RESERVE CON STREET	FED. SUPPLY CODE		NAME	AND ADDRESS	FED. SUPPLY CODE
A-B	Allen-Bradley Corp Milwaukee, WI 53204	01121	DLE		Electronics Inc.	91637
A-D	Analog Devices Inc. Norwood, MA 02026	24355	DTN	New	ettron (Consolidated) York City, NY 10013	1-04: 8
ACI	American Components, Inc. Conshohocken, PA 19428	14298	ECI	San-	tro Cube Inc. Gabriel, CA 91776	14752
AMP	Ampḥenol Broadview, IL 60153	02660	EDI	Elec Yonk	tronic Devices, Inc. ers, NY 10710	83701
APX	Amperex Elkgrove Vlg, IL 60007	73445	EFJ	Wase	. Johnson Co. ca, MN 56093	74970
BEC	Beckman Inst. Inc. Fullerton, CA 92634	73138	ERI	Erie	Technological Prod.	72982
BLD	Belden Mfg. Co. Chicago, IL 60644	70903	F-I	moun	child Inst. Corp. tain View, CA 94043	
BRG	Berg Electronics Inc. New Cumberland, PA 17070	22526	FUS	St.	man Mfg. (Fusetron) Louis, MO 63107	71400
BRN	Bourns, Inc. Riverside, CA 92507	80294	G-E	Gene Syra	ral Electric Company cuse, NY 13201	0000
BUS	Bussman Mfg. Div. St. Louis, MO 63017	71400	G-1	Gene Newa	ral Instrument Corp.	
C-I	Components, Inc. Biddeford, ME 04005	06751	GLD		d, Inc. Paul, MN 55165	52431
C -₩	Continential-Wirt El <b>ec. Corp.</b> Warminster, PA 18974	79727	H-b	Palo	ett-Packard Alto, Ca 94304	50434
CAD	Caddock Riverside, CA. 92507	19647	INT	Inte	rsil Inc. rtino, CA 9501412013 supe	32293
CAN	ITT Cannon Electric Santa Ana, CA 92702	71468	IRC	IRC Burl	Division ington, IAT 52601vs0 nonsi	1
CLB	Centralab Division Milwaukee, WI 53201	71590	L-F	Litt Des	lefuse, Inc. Plaines, IL 60016	75915
CLR	Clarostat Mfg. Co., Inc. Dover, NH 03820	12697	MOL	Mole Down	x ers Grove, IL 60515cal es	27264
стѕ	CTS Corporation Elkhart, IN 46514	71450	ТОМ	Phoe	rola Semi Prod. Inc. nix, AZ 85008	04713
DIC	Dickson Electronics Corp. Scottscale, AZ 85252	12954	NAT	Nati Sant	onal Semi Corp. a Clara, CA 95051	27014

## INSTRUCTION MANUAL Digital Multimeter

Model 4001 Model 4002

TABLE 7-1. (Cont'd)

MFR. CODE	NAME AND ADDRESS	FED. SUPPLY CODE	MFR. CODE NAME AND ADDRESS	FED. SUPPLY CODE
NCI	National Components, Inc. West Palm Beach, FL	3454 346	TEP Tepro Electric Corp. Rochester, NY 14606	02985
NEL	Northern Engr. Labs Burlington, WI 53105	00815	TPL Temple Tecate, CA 92080	29505
P&B	Potter & Brumfield Princeton, IN 47670	12300	TRW TRW Capacitor Div. Ogallala, NB 69153	84411
PAK	Paktron Vienna, VA 22180		VIS Vishay Resistor Products Malvern, PA 19355	18612
POM	Pomona Electric Pomona, CA 91766	05276	VRN Vernitron Laconia, NH 03246	13150
QTN	Q-Tron Santa Ana, CA 92705	25525	WAB Wabash-Magnetics Wabash, IN 46992	01101
R-D	Racal-Dana Instruments Irvine, Ca. 92713	21793		
RAY	Raytheon Company Quincy, MA	94144		
RCA	RCA Corporation Moorestown, NJ 08050	02734		
RCL	RCL Electronics, Inc. Manchester, NJ 03102	01686		
SIE	Siemens Corporation Iselin, NJ 08830	25088		
SIG	Signetics Corp. Sunnyvale, CA 94086	18324		
SIL	Siliconix Inc. Santa Clara, CA 95054	17856		
SPG	Sprague Electric Co. Visalia, CA 93278	14659		
SOL	Solitron Devices Inc. San Diego, CA 92123	22229	E F	
STD	Standard Condensor Chicago, IL	97419	r o	
T-1	Texas Instruments, Inc. Dallas, TX 75231	01295		
TEL	Tel Labs Manchester, NH 03102	94322	- BQ ts	

# INSTRUCTION MANUAL Digital Multimeter Model 4002

### REPLACEABLE PARTS LIST

	Part No.		Lode	PC-Board Lack:lan	Sch.				10001
Circuit Desig.	Description	11681	1-7	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.	-Bis
	81 TR	N 1 6H1	1-7	E-2	£-L		700A. 75V .		010
	5-4 FA	2039	103		RS (C)	7 101	11 Flat, SA, 5	Brirue Rec	11:1
		TIENT III			1. 28992E-Po		3 50V	15 1.130A	112
		11,540		(PL-Board 4	148-Pg. 7-11	,	V25 , Acc.	3 1.39	811
101	4.7µF, 20V, ETT .		150	D-750	D-4	NC1	KNS475A020K	C-179-4.7	
102	4.7µF, 20V, ETT .				D-4	NCI	KNS475A020K	C-179-4.7	
103	470µF, 35V, EAL .				D-4	NIC	35ELA470	C-289-470	
104	470µF, 35V, EAL .				E-4 d-3	NIC	35ELA470	C-289-470	105
105	0.14F, 1000V MPF				F-4 1-3	STD	M2W-F-0.1µF	C-2851	508
106	.25-1.5pF, 2000V,				F-4		273-101	C184	
107	1000pF, 500V, ±5				F-4 6-3	CLB	CPR-1000	C-138-1000	AGE.
108	2200µF, 15V, EAL				D-3 8-3	NAC	16FLA2200	C-290-2200	305
109	3.3pF, ±0.5pF, 5				E-3	NAC	DT200-3R3	C-291-3.3P	
110	1µF, 100V, ±10%,	T-61 T-!		G-2	E-3	POT EFJ	4309C-105K 273-1-1	C-294-1 C-184	
112	.25-1.5pF, 2000V,	Trimme	er	0-2 (80) 8			187-0109-005	C-284	
113	1.9-15.8pF, 250V, 1100pF, 500VDC,						RDM19FD112F03	C-278-110P	
114	1100pF, 500VDC, ±				# <b>E</b> 63⊾08-39) F-3	G-1	RDM19FD111F03	C-278-1100	
115	33μF, 15V, ETT				F 2	NCI	KNS336D015K	C-228-33	
116	33µF, 15V, ETT				E-2 /52	NCI	KNS336D015K	C-228-33	
117	1μF, 100V, ±10%,				D-2	POT	4309C-105K	C-294-1	-01.51
118	.22µF, 200VDC, ±				D-2 T-M	POT	22-200-10-X363UW	C-26922	
119	NOT USED	FH0560	1-7	0-2	H-7		Lip 0 033	19 933 12	20:
120	luf, 100V, ±10%,			F-3 5-0	D-1 7-M	POT	4309C-105K	C-294-1	
121	4.7µF, 20V, ETT .	610560	1-1	D-8 E-0	D-2 T-L	NCI	KNS475A020K	C-179-4.7	
122	4.7µF, 20V, ETT .				D-2	NCI	KNS475A020K	C-179-4.7	2023
123	.lμF, 200V, 20%,				E-2	ECI	625B1C104	C-2211	
124	4μF, 100V, 20%, M				E-1	POT	0109-5432	C-294-4	
125	100pF, 1000V, Cer				29 <b>652</b> Harð) seineð	CLB	DD-101	C-64-100P	
	FU-13	312002		(PC-Board	th. 28992E-F 451-Pg. 7-1	13)	250V, 3AE		
301	4.7µF, 35V, EAL	(01.	, 'B	F-17 -0 -13	E-3	NCI	KNS475A020K	C-179-4.7	
302	4.7µF, 35V, EAL .		- 114	F-7	E-3	NCI	KNS475A020K	C-179-4.7	
303	1.0µF, 250V, MPY.	312002		F-7 (-3	E-3	AMP		C-256-1	105
304	100μF, 35VC			F-8	F-4	NIC	35-ULA-100	C-295-100	
305	100µF, 35VC	(0)-	. ph-	F-8	2 F-301302	NIC	35-ULA-100	C-295-100	
				DIODES	1808-39)				
	Part No	do basig.	'100''	Series (Sc	th. 28992E-F 448-Pg. 7-		) no!	Descript	p: 290
ircuit esig.			OH HD	Sch. Location	PC-Board Location	Mfr. Code	Mfr. signA Idpi Desig. signA Idp	Part No.	10011
8101	0-14- p 161	1000	0.91	5-0	8-8	ED.	0010	05-26	11003
R101	Bridge Rectifier, Bridge Rectifier,	100V, ZA	01		D-5 8-0 D-5 2-A	ED1	PD10 PF40	RF-36 RF-46	1001
R102 R103	Silicon Rectifier,				F-5 E-A	T-1	111/1007	DE 50	
R104	Silicon Rectifier			E-3	F-5	T-1	IN4007	RF-50	90011
R105	Silicon Rectifier				F-5	T-1	IN4007	RF-50	1001
R106	Rectifier, 75mA,	75V		D-8	D-3	T-1	IN914	RF-28	
R107	Rectifier, 75mA,	75V		J-2		T-1	11914	RF-28	
1108	Rectifier, 75mA,	750	187	J-2	C-2	3 <b>0T−1</b>	IN914	RF-28	
		. + 421 - +1		1 148-50. 7 PC-8dard					
	Part No	ode Sesig	.0	ng 1820J	Location		ngi		0.05 Lg
	95-78	T0000-10 T0		0-3	8-8		Type	EV Read	1019
	RL-56	T6009-10 TO		4-3	8-8		Type		\$01x
		07 UF-40102 07 UF-40102		1-3	8-A		- agyT t		KIDS
	10 M			8-7	8-A		1 Type		KIDA
		20104-90 70	-2	4-7	8-A			SV. Ree	20 X

### DIODES (CR) (CON'T)

		707 794				
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
CR109 CR110 CR111 CR112 CR113	Rectifier, 75mA, 75V	J-3 A-3 2 22 A-4 = 641	E-2 E-2 G-2 G-2 C-2	T-I T-I EDI SOL T-I	1N914 1N914 PE05 3A50 1N914	RF-28 RF-28 RF-48 RF-34 RF-28
	"300"	Series (Sch (PC-Board 4				
CR301 CR302 CR303 CR304 CR305	Rectifier, 75mA, 75V	F-7 F-7 E-8	D-4 E-3 E-3 F-4 F-4	T-! T-! T-! T-! T-!	11914 11914 11914 11914 11914	RF-28 RF-28 RF-28 RF-28 RF-28
	"200"	DISPLAY Series (Sch (PC-Board 4	. 28992E-Pg			
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
DS 201 DS 202 DS 203 DS 204 DS 205	±1 LED Digit	H-7 H-7 J-7	C-2 D-2 D-2 D-2 E-2	F-1 F-1 F-1 F-1	FND561 FND560 FND560 FND560 FND560	DD-17 DD-16 DD-16 DD-16 DD-16 DD-16
		FUSES Series (Sch (PC-Board 4	. 28992E-Pg			
Circuit Desig.	Description encoded the	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
F101 F102	Fuse, Slo-Blo, 1/8A, 250V, 3AG Fuse, 2A, 250V, 3AG		D-4 F-3	BUS	MDL 312002	FU-20 FU-13
		Series (Sch (PC-Board 4				
F301	2A, 250V, 3AG, Quick	F-6 CONNECTO	C-3 RS (J)	L-F	312002	FU-13
	''100'	Series (Sc			)	
Circuit Desig. ·	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
J1001 J1002 J1003 J1004 J1005 J1006 J1007	11-Pin Right Angle	H-6 B-8 D-8 A-5	B-3 F-3  D-5	MOL MOL MOL POM POM	22-15-2111 22-15-2061 2139-3 2139-8 1581	CS-348-2 CS-348-1 CS-287-3 CS-287-8 8J-11-0 BJ-11-2
	. "100"	RELAYS Series (Sc (PC-Board			)	
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
K101 K102 K103 K104 K105	5V, Reed Type	B-8 A-8 A-8	E-4 E-4 E-4 F-4	COT COT COT COT	UF-40097 UF-40097 UF-40102 UF-40102 UF-40102	RL-56 RL-56 RL-59 RL-59 RL-59

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## CONNECTORS (P) "1000" Series (S@h. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
P1001	6-Pin	. н-7	G-5	MOL	22-03-2061	CS-247-1
P1002	11-Pin	. н-6	G-2	MOL	22-03-2061	CS-347-2
P1003	MALE	. в-8	C-5	MOL	A-2391-3A	cs-288-3
P1004	MALE	D-5	D-3	MOL	A-2391-8A	cs-288-8
P1005	NOT USED				5.5	
P1006	NOT USED					
P1007	Line Cord	. в-8		R-D		co-9

## TRANSISTORS (Q) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
0101	N-Chan, JFET	. D-3	E-4	INT	ITE4392	TG-77
0102	N-Chan, JFET		E-4	INT	ITE4392	TG-77
0103	N-Chan, JFET		C-2	INT	ITE4392	TG-77
0104	N-Chan, JFET		C-2	INT	ITE4392	TG-77
0105	N-Chan, JFET		D-2	INT	ITE4392	TG-77
0106	N-Chan, JFET	. G-3	E-2	R-D		TG-128
Q107	N-Chan, JFET	. G-4	E-2	R-D		TG-128
Q108	NPN, Switch	. н-6	G-2	MOT	2N3904	TG-47
0301	Thursday CCD		451-Pg. 7-13		10(5)	T0 120
Q301	Thyristor, SCR		C-4	MOT	106F1	TG-132
Q302	NPN, Switch		C-3	MOT	2N3904	TG-47
0303	NPN, Switch		D-4	MOT	2N3904	TG-47
0304	NPN, Switch		E-4	MOT	2N3904	TG-47
Q305	NPN, Switch		E-4	MOT.	2N3904	TG-47
Q306	PNP, Silicon, TO-92 Case· · · ·		E-4	R-D		TG-53
0307	NPN, Switch		E-3	MOT	2N3725	TG-131
Q308	NPN, Switch · · · · · · · · · · · · · · · · · · ·		E-3	MOT	2N3725	TG-131
Q309	PNP, Silicon·······		D-3	TOM	MPS-WA5	TG-133
Q310	PNP, Silicon, TO-92 Case	. E-6	D-3	R-D		TG-53

## RESISTORS (R) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit	D	Sch. Location	PC-Board	Mfr.	Mfr.	
Desig.	Description	Location	Location	Code	Desig.	Part No.
RIOI	1MΩ, ±0.5%, 2W, MtF	. D-3	E-5	ACI	PME80T9	R-267-1M
R102	9.88M, ±0.5%, .5W, 1200V, MtF	D-2	E-4	TRW	AR90T10	R-265-9.88M
R103	200KΩ, 10%, Cermet Trimmer	C-2	E-5	BEC	89P	RP-89-200K
R104	100KΩ, 10%, 2W, Comp	E-4	F-5	A-B	HB	R-2-100K
R105	47KΩ, 10%, 2W, Comp	G-4	F-5	A-B	нв	R-3-47K
R106	47KΩ, 10%, 2W, Comp	D-4	G-5	A-B	НВ	R-3-47K
R107	100Ω, 10%, Cermet Trimmer	. J-1	C-3	BRN	30-69-P	RP-64-100
R108	200Ω, 10%, Cermet Trimmer		C-3	BRN	30-69-P	R-64-200
R109	MATCHED SET WITH VR105	J-1	C-3	TRW	MAR-5	R-263-99.8K (28798A)
R110	MATCHED SET WITH VRIOS		C-3	TRW	MAR-5	R-263-4.59K (28798A)
R111	931Ω, 1%, 1/8W, MtF		C-3	IRC	CEA-TO-931	R-88-931
R112	50KΩ, 10%, Cermet Trimmer	E-3	D-3	BEC	72PMR	RP-97-50K
R113	200KΩ, 1%, 1/8W, MtF		D-3	IRC	CEA-TO-200K	R-88-200K
R114	1.8M, 10%, 1/4W, Comp	D-3	D-3	MEP	CR25, 5%	R-76-1.8M
R115	100Ω, 1%, 1/8W, MtF	D-3	D-3	IRC	CEA-TO-100	R-88-100

## RESISTORS (R) (CON'T) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
Desig.  R116 R117 R118 R119 R120 R121 R122 R123 R124 R125 R126 R127 R128 R129 R130 R131 R132 R131 R132 R133 R134	4.99KΩ, .1%, 1/10W, MtF. 44.9KΩ, .1%, 1/10W, MtF. 998KΩ, .1%, 1/4W, MtF. 99.8KΩ, .1%, 1/4W, MtF. 9.98KΩ, .1%, 1/10W, MtF. 1.002KΩ, .1%, 1/10W, MtF. 270KΩ, 10%, 1/4W, Comp898Ω, .1%, 5W, WW1Ω, .1%, 7.5W, WW, 5-Terminal 120Ω, 10%, 1/4W, Comp. 5KΩ, 10%, Cermet Trimmer. 50Ω, 10%, Cermet Trimmer. 50Ω, 10%, Cermet Trimmer. 143KΩ, .1%, 1/10W, MtF. 100KΩ, 1%, 1/10W, MtF. 11KΩ, 1%, 1/8W, MtF. 17hick Film Network. 47KΩ, 10%, WW, Comp. 9Ω, 0.5W, 0.1%, WW. 90ΩΩ, 0.1%, 1/2W, MtF. 11KΩ, 1%, 1/8W, MtF. 11KΩ, 1%, 1/8W, MtF. 19.6KΩ, 1%, 1/8W, MtF. 19.6KΩ, 1%, 1/4W, Comp. 9ΩΩ, 1/2W, 1%, MtF. 19.6KΩ, 1%, 1/4W, Comp. 20K, 10%, Cermet Trimmer. 50Ω, 10%, Cermet Trimmer. 50Ω, 10%, Cermet Trimmer. 20KΩ, 10%, Cermet Trimmer. 20KΩ, 10%, Cermet Trimmer. 3.5KΩ, 5%, 1/4W, Comp. 22M, 10%, 1/4W, Comp. (Part of 28798A). (Part of 28798A).	Location  E-2 E-2 D-2 D-2 D-1 D-1 B-4 B-4 G-7 D-2 D-2 D-2 L-1 J-2 J-2 J-2 J-3 SEVERAL F-4 B-4 C-4 B-4 F-5 F-5 D-3 E-3 F-3 J-1				Part No.  R-263-4.99K R-263-44.9K R-263-99.8K R-263-99.8K R-263-99.8K R-263-1.002K R-76-270K R-232898 R-2621000 RP-97-50 R-263-143 R-263-856 R-88-100K R-88-26.7K R-88-3.01K TF-65 R-3-47K R-252-9 R-169-90 R-88-11K R-88-19.6K RP-97-50K
R201 R202	"200" 120Ω, 1/4W, Comp	(PC-Board	1. 28992E-Pg 448-Pg. 7-1 C-2 E-2		CR25, 5% 899-3-R47	R-76-120 RF-64
	''300''		1. 28992E-Pg 451-Pg. 7-1			
R301 R302 R303 R304 R305 R306 R307 R308 R309 R310 R311 R312 R313 R314 R315 R316	20ΚΩ, 0.5W, POT 330Ω, 10%, 1/4W, Comp 1ΚΩ, 10%, 1/4W, Comp 3.9ΚΩ, 20%, 3W, WW 4.7ΚΩ, 10%, 1/4W, Comp 33ΚΩ, 10%, 1/4W, Comp 82ΚΩ, 10%, 1/4W, Comp 82ΚΩ, 10%, 1/4W, Comp 10Ω, 10%, 1/4W, Comp 10Ω, 10%, 1/4W, Comp 10Ω, 10%, 1/4W, Comp 100ΚΩ, %%, 1/4W, Comp 100ΚΩ, %%, 1/4W, Comp 6.8ΜΩ, 5%, 1/4W, Comp 6.8ΜΩ, 5%, 1/4W, Comp 576ΚΩ, 1%, 1/8W, Comp 576ΚΩ, 1%, 1/8W, Comp	E-7 E-6 E-7 D-8 F-7 D-8 D-8 D-8 E-6 F-6 F-6 F-6	C-3 C-3 D-4 D-4 D-3 C-3 E-3 E-3 E-3 E-3 E-3 E-3 E-3 E-3	BEC MEP TEP MEP A-B MEP A-B MEP MEP MEP MEP IRC	72PMR-20K CR25, 5% CR25, 5% TS3 CR25, 5% CB-332-10% CR25, 5% CR25, 5% CR-100-10% CR-25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5% CR25, 5%	RP-97-20K R-76-330 R-76-1K R-268-3.9 R-76-4.7K R-76-3.3K R-76-82 R-76-82 R-76-10 R-76-100K R-76-100K R-76-6.8M R-76-6.8M R-88-576K R-88-500K
01	"100"	(PC-Board	h. 28992E-Pg 448-Pg. 7 <b>-</b> 1	1)		
Circuit Desig.	Description	Sch. Location	PC-Board Location		Mfr. Desig.	Part No.
\$101 \$102 \$103	Line Voltage Selector	C-6	C-4 D-4 G-4	R-D	GG350PCDPDT  	SW-318 SW-397 SW-402 (27696A)

### **INSTRUCTION MANUAL** Digital Multimeter Model 4002

V PET SHENCE ---

	TRA	SFORM	ERS (T)		
"100"	Series	(Sch.	289928	-Pg.	7-10)
	(PC-E	Board	448-Pg.	7-11	)

ircuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
101	Transformer, Power (100/200 V)	C-6	D-5 D-5	R-D R-D		TR-168 TR-169
			h. 28992E-P			
	wins ours item		d 451-Pg. 7-			
r301	Transformer, Power	F-7	F-3	R-D		TR-170
	7 m					
	11.00 M		INTS (TP) E-Pg. 7-10)			
	- No.		. •			
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
TPI	Test Point	r-3		R-D		
TP2	Test Point	. c-8		Ā		
TP3	Test Point					
TP4 TP5	Test Point					
TP6	Test Point			ļ		
TP7	Test Point					
TP8 TP9	Test Point			Ţ		
TP10	Test Point			R-D		
		1000				-1
	''100'	Series (S	CIRCUITS ( ch. 28992E- d 448-Pg. 7-	Pg. 7-10	))	
Circult		Sch.	PC-Board	Mfr.	Mfr.	
Desig.	Description	Location	Location	Code	Desig.	Part No.
U101 U102	Operational AMP 8-Pin, TO-5 Operational AMP 8-Pin, DIP		E-3 D-3	NAT	LH0022CH LM301AN	1C-165 1C-167
U102 U103	4-1/2 Digit Analog-Processor		D-2	INT	8052A	LSI-12
J104	Operational AMP 8-Pin, TO-5	H-2	E-2	NAT	LH0042CH	IC-175
U105	TRMS Converter		D-2	A-D	AD536J	IC-172
J106 J107(A-D)	4-1/2 Digit Logic Processor 4011 CMOS Unbuffered,		E-2 F-2	MOT	7103A MC14011CP	LSI-11 IC-102
	''200'		ch. 28992E-		))	
			d 449-Pg.7-			
U201 U202	Segment Drive		F-2 F-2	MOT T-I	4511 75492	1C-168 1C-169
2					75452	10 .05
	''300'		ch. 28992E-1 rd 451-Pg. 7		))	
U301	Dual D-Type Flip-Flop, 14-Pin DIP .		D-3	RCA	CD4013AE	IC-103
J302	1.1V Micro-Power Detector	r - 0	E-3	INT	ICL8211CPA	IC-177
		Series (Sch	JLATORS (VR) n. 28992E-Po 148-Pg. 7-11	g. 7-10)		
	"100"	Series (Sch (PC-Board A	n. 28992E-Po 448-Pg. 7-11 PC-Board	g. 7-10)  )   Mfr.	Mfr.	
Desig.		Series (Sch (PC-Board A	PC-Board Location	g. 7-10) !)		Part No.
Desig. VR101	"100"  Description -15V, 3-Term	Series (Sch (PC-Board A Sch. Location	n. 28992E-P6 448-Pg. 7-11 PC-Board Location E-5	Mfr. Code	Mfr. Desig. MC7915CT	1C-174
Circuit Desig. VRIO1 VRIO2 VRIO3	Description  -15V, 3-Term	Series (Sch (PC-Board A Sch. Location C-8 C-8	n. 28992E-Po #48-Pg. 7-11 PC-Board Location E-5 E-5	g. 7-10) 1) Mfr. Code	Mfr. Desig.	
Desig. VR101 VR102 VR103 VR104	Description -15V, 3-Term	Series (Sch (PC-Board A Sch. Location C-8 C-8	n. 28992E-PG 448-Pg. 7-11 PC-Board Location E-5 E-5	Mfr. Code MOT MOT	Mfr. Desig. MC7915CT MC78L15CP MC7805CT	1C-174 1C-170 1C-93
/R101 /R102 /R103 /R104	Description  -15V, 3-Term	Series (Sch (PC-Board A Sch. Location C-8 C-8	n. 28992E-Po #48-Pg. 7-11 PC-Board Location E-5 E-5	Mfr. Code MOT	Mfr. Desig. MC7915CT MC78L15CP	1C-174 1C-170
Desig. VRIO1 VRIO2	Description  -15V, 3-Term	Series (Sch. Location  C-8 C-8 D-7 J-2 Series (Sch	n. 28992E-PG 448-Pg. 7-11 PC-Board Location E-5 E-5	Mfr. Code  MOT MOT R-D	Mfr. Desig. Mc7915CT Mc78L15CP Mc7805CT	1C-174 1C-170 1C-93
Desig. VRIO1 VRIO2 VRIO3 VRIO4 VRIO5	Description  -15V, 3-Term	Series (Sch. Location  C-8 C-8 D-7 J-2  Series (Sch. (PC-Board	28992E-P6 48-Pg. 7-11  PC-Board Location  E-5 E-5 D-4 C-2 28992E-P6	Mfr. Code  MOT MOT R-D	Mfr. Desig. Mc7915CT Mc78L15CP Mc7805CT	1C-174 1C-170 1C-93
Desig. VRIO1 VRIO2 VRIO3 VRIO4 VRIO5	Description  -15V, 3-Term	Series (Sch. (PC-Board 4 Sch. Location  C-8 C-8 D-7 J-2 Series (Sch. (PC-Board E-6  CRYST Series (Scries (Sch.	28992E-Pc 448-Pg. 7-11  PC-Board Location  E-5 E-5 D-4 C-2 28992E-Pc 451-Pg. 7-1	Mfr. Code  MOT MOT R-D  3. 7-10)  MOT R-D  MOT R-D  MOT R-D  MOT R-D	Mfr. Desig. MC7915CT MC78L15CP MC7805CT	IC-174 IC-170 IC-93 (28798A)
Desig.  VRIO1 VRIO2 VRIO3 VRIO4 VRIO5	Description  -15V, 3-Term	Series (Sch. (PC-Board 4 Sch. Location  C-8 C-8 D-7 J-2 Series (Sch. (PC-Board E-6  CRYST Series (Scries (Sch.	28992E-Pc 448-Pg. 7-11  PC-Board Location  E-5 E-5  D-4 C-2  1. 28992E-Pc 451-Pg. 7-1  C-3  TAL (Y) h. 28992E-Pc	Mfr. Code  MOT MOT R-D  3. 7-10)  MOT R-D  MOT R-D  MOT R-D  MOT R-D	Mfr. Desig. MC7915CT MC78L15CP MC7805CT	IC-174 IC-170 IC-93 (28798A)
Desig. VR101 VR102 VR103 VR104	Description  -15V, 3-Term	Series (Sch. (PC-Board A Sch. Location C-8 C-8 D-7 J-2 Series (Sch. (PC-Board E-6 CRYST Series (Sc (PC-Board	28992E-Pc 448-Pg. 7-11  PC-Board Location  E-5 E-5  D-4 C-2 28992E-Pc 451-Pg. 7-1  C-3  AL (Y) h. 28992E-Pc d. 448-Pg. 7	Mfr. Code  MOT MOT R-D  3)  MOT Pg. 7-10	Mfr. Desig. Mc7915CT Mc78L15CP Mc7805CT	IC-174 IC-170 IC-93 (28798A)

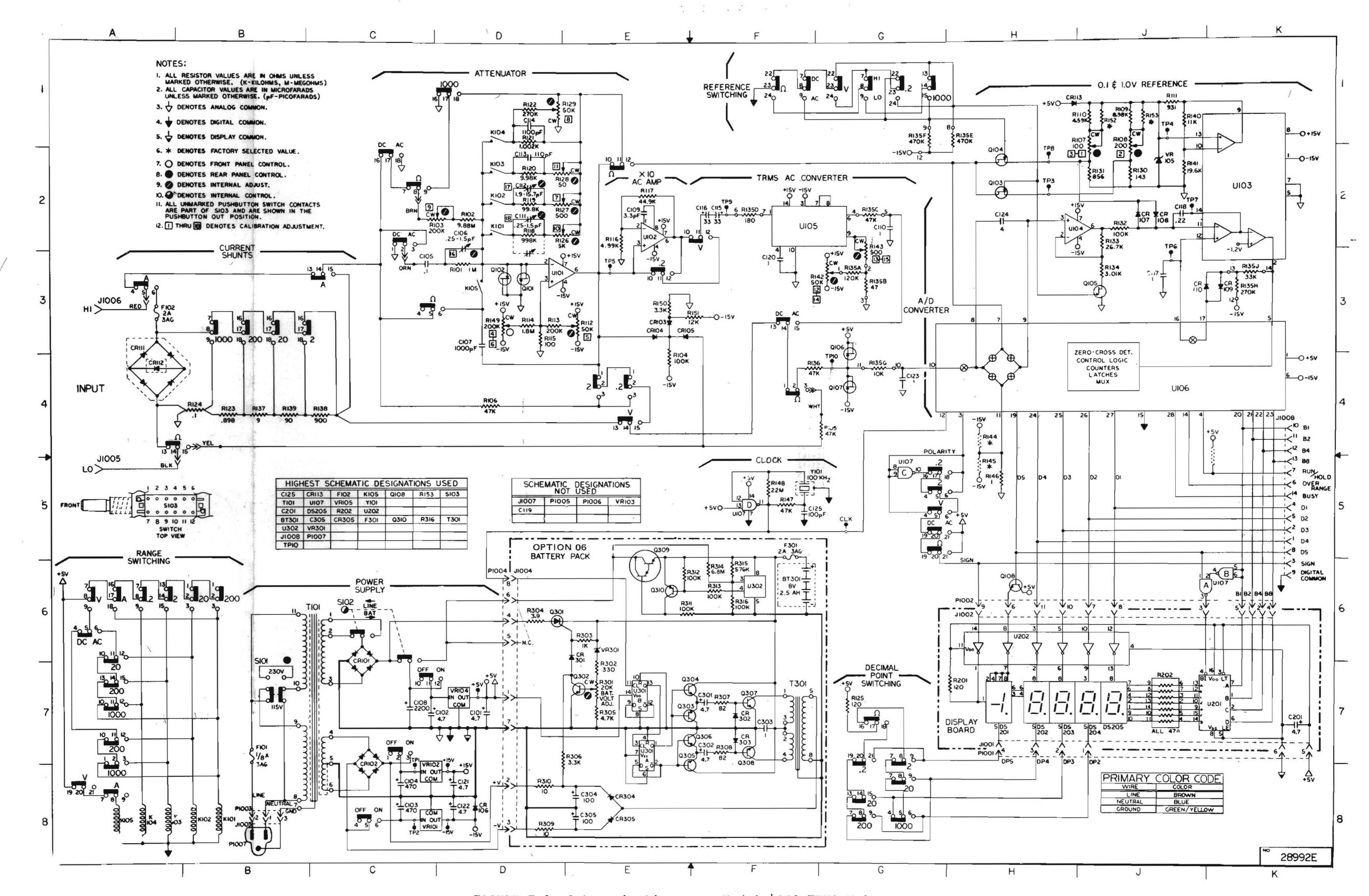


FIGURE 7-1 Schematic Diagram - Model 4002 TRMS Multimeter.

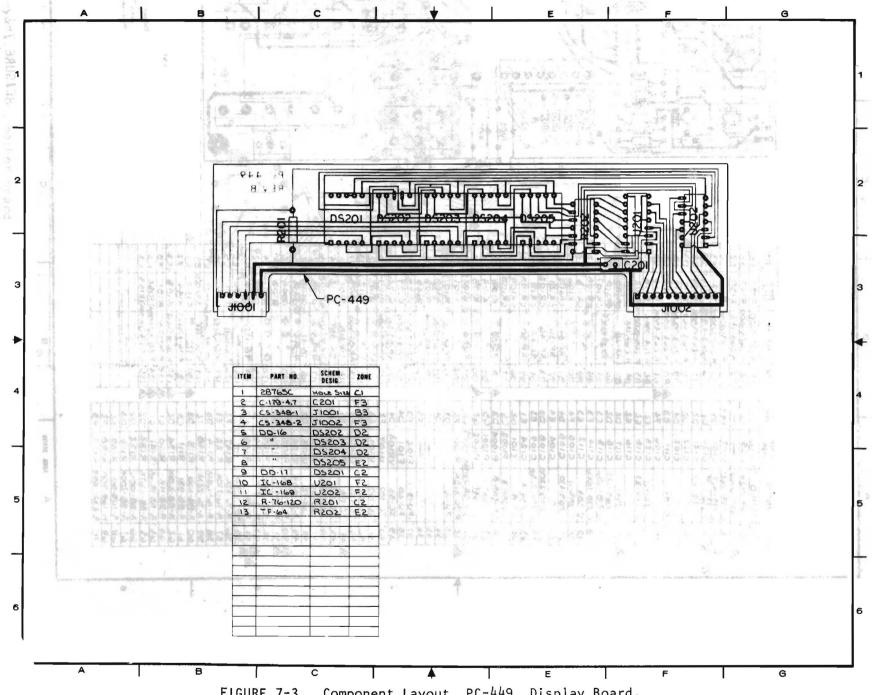


FIGURE 7-3. Component Layout, PC-449, Display Board.

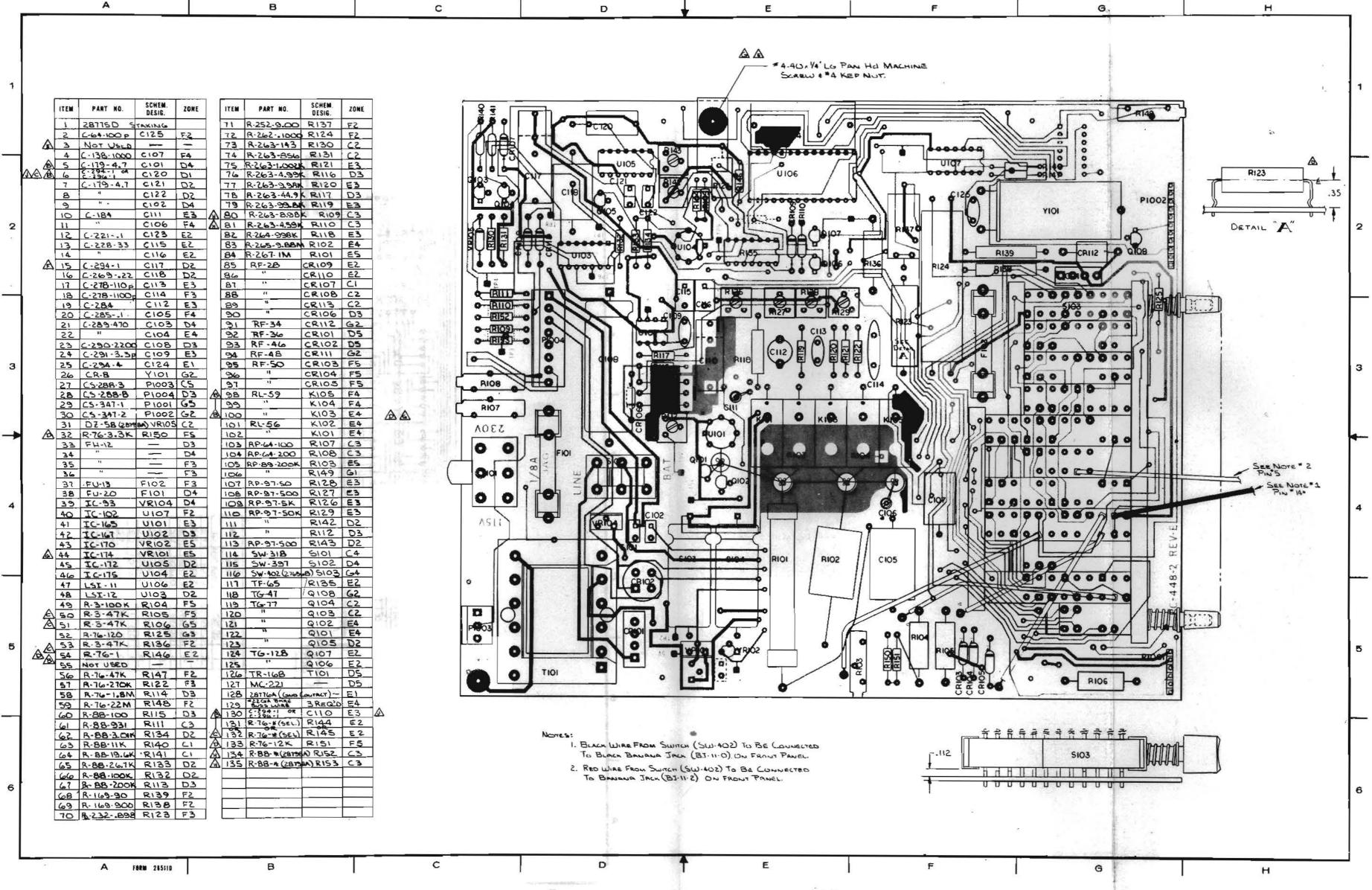


FIGURE 7-2. Component Layout, PC 448, Mother Board.

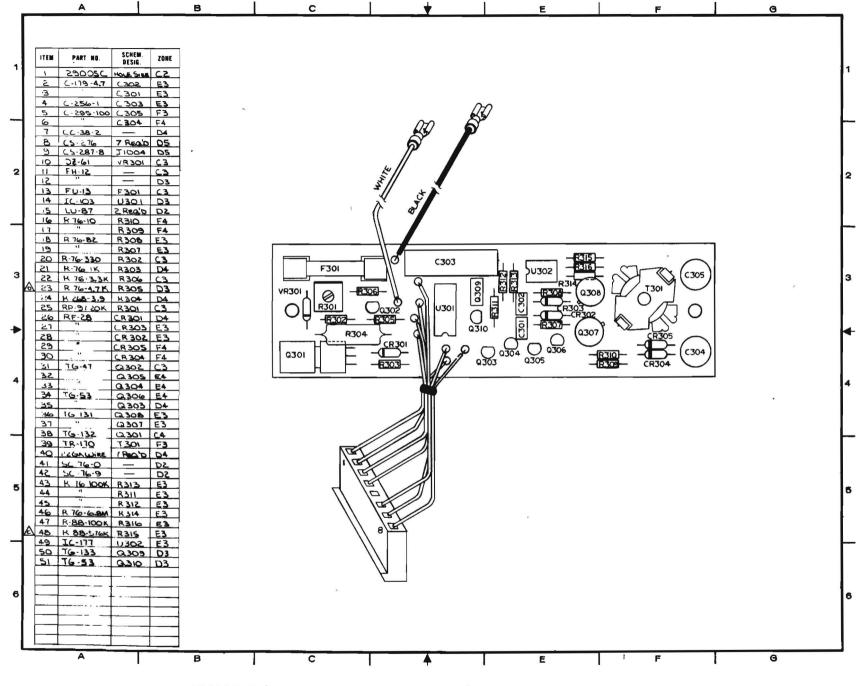


FIGURE 7-4. Component Layout, PC-451, Battery Pack Board.