

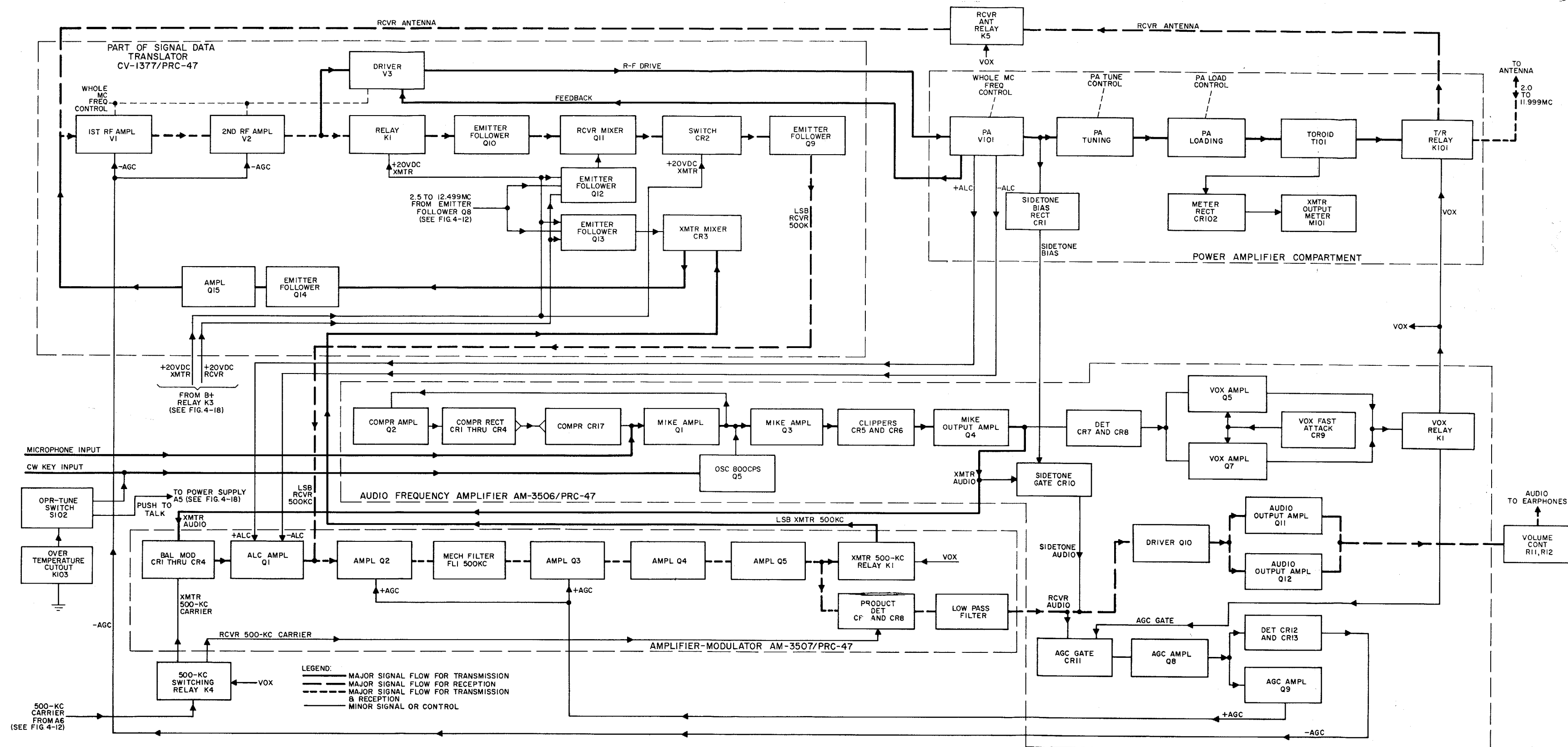


Figure 4-3: Transmit-Receive Circuits, Detailed Block Diagram