

TERMINATED COAXIAL SPDT and DP3T up to 18 GHz - RAMSES Concept

R585

Connectors RF

3 : SMA up to 3 GHz4 : SMA up to 18 GHz

Type

Failsafe
 Failsafe + I.C.
 Latching

4 : Latching + I.C.
5 : Latching + S.C.O.(1)
6 : Latching + S.C.O. + I.C.(1)

7 : Normally Open8 : Normally Open + I.C.

Actuator voltage

2 : 12 Vdc 3 : 28 Vdc

I.C. : Indicator contact.
S.C.O. : Self Cut Off

Actuator terminal

0 : Solder Pins

Options

0 : Without option

1 : Positive common (2)(4)3 : With suppression diodes (1)4 : With suppression diodes and

positive common (3)(4)

TTL and termination options

0 : Without TTL driver (DP3T)1 : With TTL driver (DP3T)

2 : Terminated (internal termination) / Without TTL driver

3 : Terminated (internal termination) / With TTL driver
 4 : Terminated (external termination) / Without TTL driver

5 : Terminated (external termination) / With TTL driver

(1) : Suppression diodes are already included in Self Cut-off and TTL (2) : TTL option is not available with this option

(3): Self Cut-off and TTL options are not available with this option
 (4): Positive common must be specified only with Type 3, 4, 5 or 6

GENERAL SPECIFICATIONS - RF PERFORMANCES

Connectors		SMA					
Frequency range	GHz	DC - 3 / DC - 18					
		DC - 3	3 - 8	8 - 12.4	12.4 - 18		
V.S.W.R. (max)		1.20	1.30	1.40	1.50		
Insertion Loss (max)	dB	.20	.30	.40	.50		
Isolation (min)	dB	80	70	60	60		
Impedance	Ω	50					
Switching sequence		Break Before Make					

GENERAL SPECIFICATIONS - ADDITIONAL SPECIFICATIONS

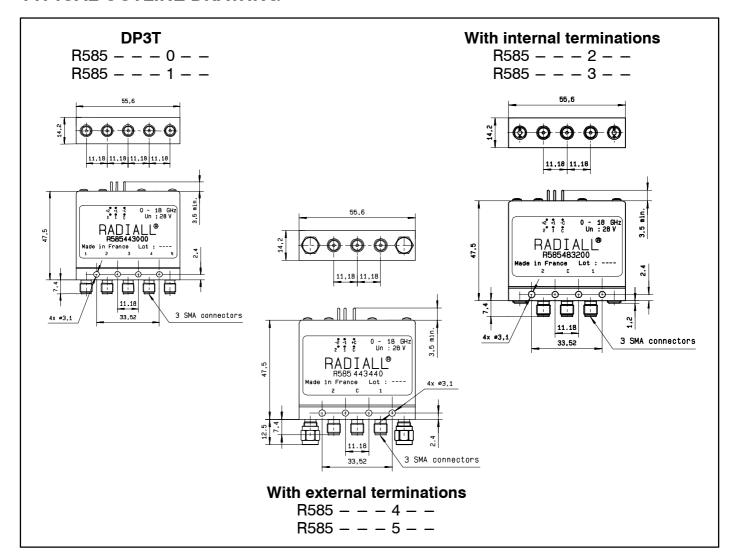
Operating mode		Failsafe, Latching or Normally open							
		Failsafe		Latching		Normally open			
Actuator voltage (nominal)	Vdc	12	28	12	28	12	28		
Coil resistance (±10%)	Ω	24	138	29	175	47.5	275		
Operating current at 23°C	mA	500	205	420	160	250	102		
Average power		See Power Rating Chart page 9							
TTL INPUT	High level Low level	2.2 to 5.5 V 0 to 0.8 V							
Peak power	kW	1.5 (1 μs, 1 ‰)							
Indicator rating		1 W - 30 V - 100 mA							
Switching time (max)	ms	10							
Life		2 10 ⁶ cycles for products with internal terminations 10 10 ⁶ cycles for all other products							
Connectors		SMA							
Actuator terminals		Solder pins							
Operating temperature range	$^{\circ}\!C$	-40, +85							
Storage temperature range	$^{\mathcal{C}}$	-55, +85							



COAXIAL DP3T up to 18 GHz - RAMSES Concept



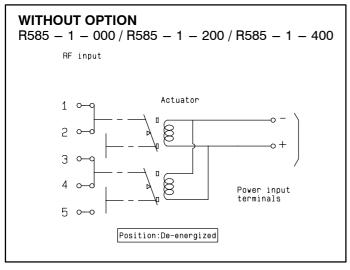
TYPICAL OUTLINE DRAWING

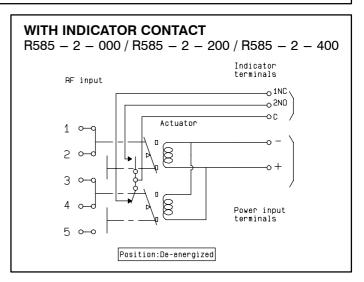


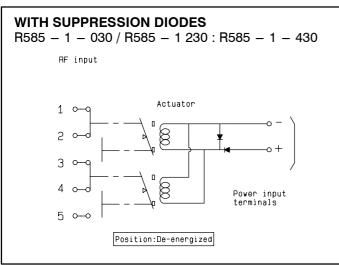
TECHNICAL INFORMATION

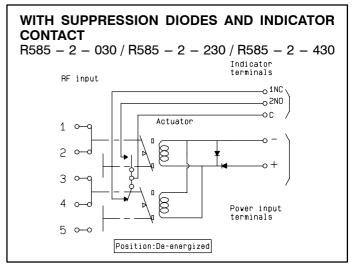
SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

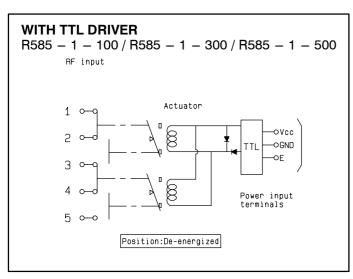
FAILSAFE

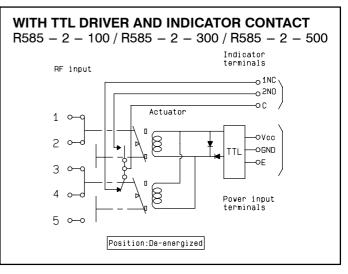










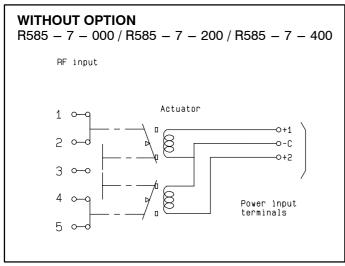


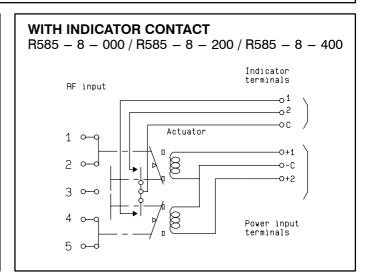


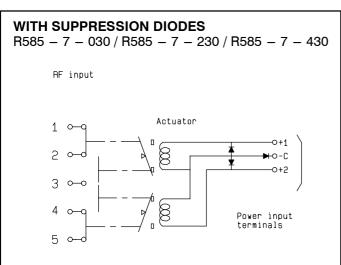
TECHNICAL INFORMATION

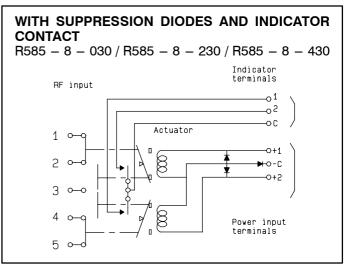
SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

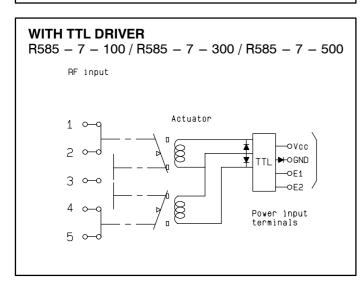
NORMALLY OPEN

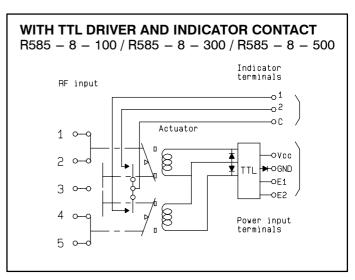










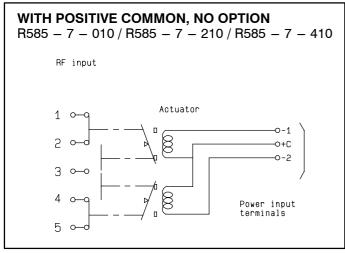


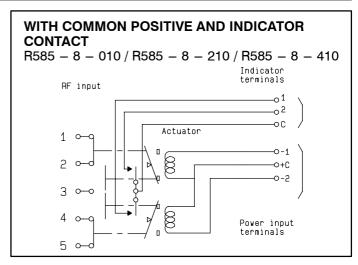


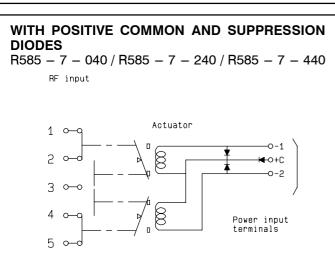
TECHNICAL INFORMATION

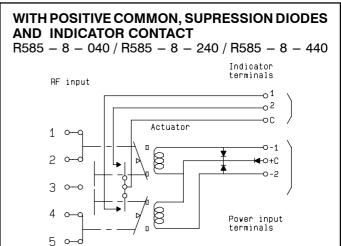
SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

NORMALLY OPEN

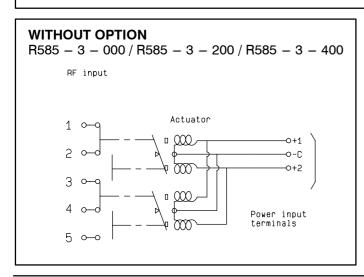


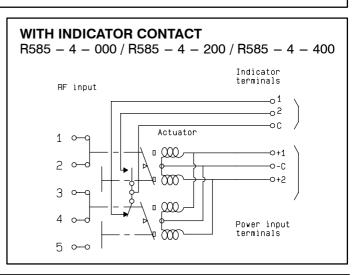






LATCHING



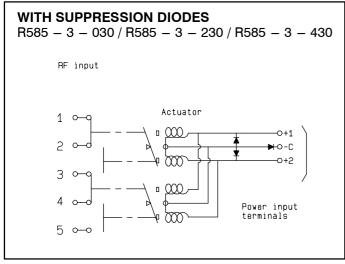


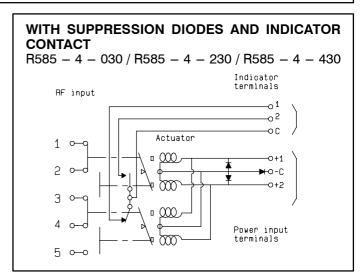


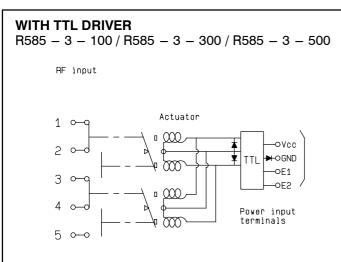
TECHNICAL INFORMATION

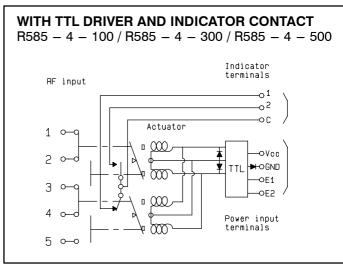
SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

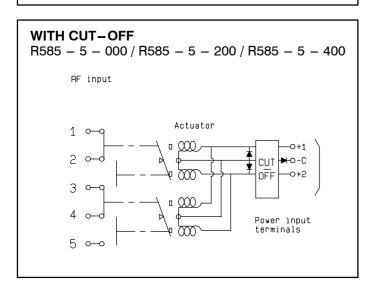
LATCHING

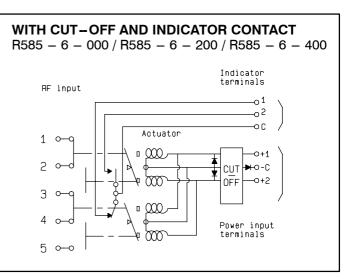










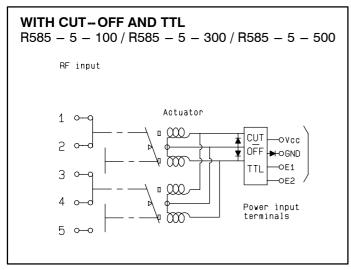


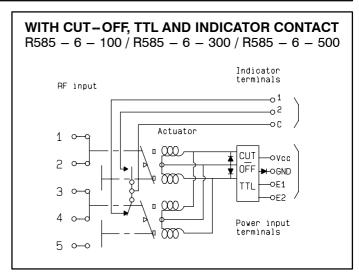


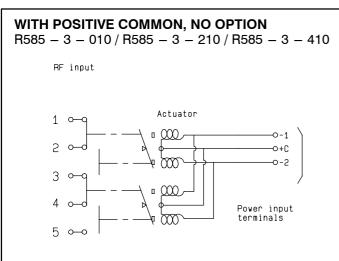
TECHNICAL INFORMATION

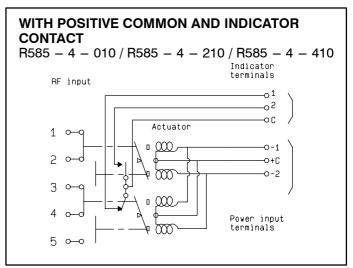
SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

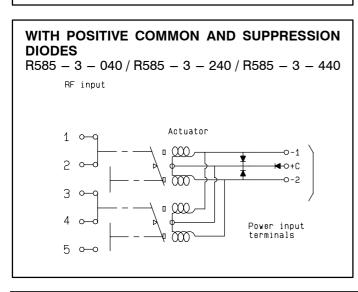
LATCHING

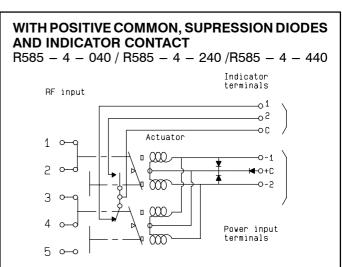










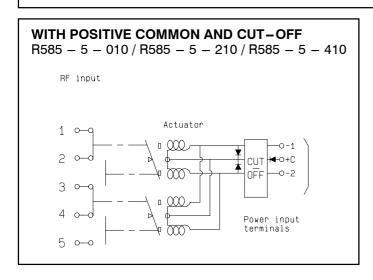


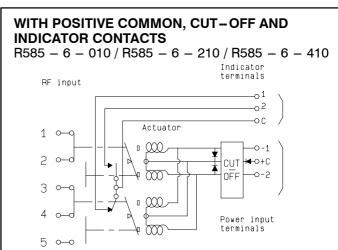


TECHNICAL INFORMATION

SCHEMATICS OF ELECTRICAL CONNECTION FOR **DP3T** SWITCHES:

LATCHING





PIN IDENTIFICATION

	Pin							
Туре	1	2	3	4	5	6	7	8
Failsafe	+		-					
Failsafe + I C.	+		-			2NO	1NC	С
Failsafe + TTL	Е		Gnd	VCC				
Failsafe + I.C. + TTL	Е		Gnd	VCC		2NO	1NC	С
Latching Latching + Cut-off	-2 +2	-1 or+1	+C -C					
Latching + I.C. Latching + I.C. + Cut-off	-2 +2	-1 or+1	+C -C			2	1	С
Latching + TTL. Latching + TTL + Cut-off	E2	E1	Gnd	VCC				
Latching + TTL.+ I.C. Latching + TTL + I.C. Cut-off	E2	E1	Gnd	VCC		2	1	С
Normally Open N O + Cut-off	-2 +2	-1 or+1	+C -C					
N O + I.C. N O + I.C. + Cut-off	-2 +2	-1 or+1	+C -C			2	1	С
N O + TTL. N O + TTL + Cut-off	E2	E1	Gnd	VCC				
N O + TTL.+ I.C. N O + TTL + I.C.+ Cut-off	E2	E1	Gnd	VCC		2	1	С

