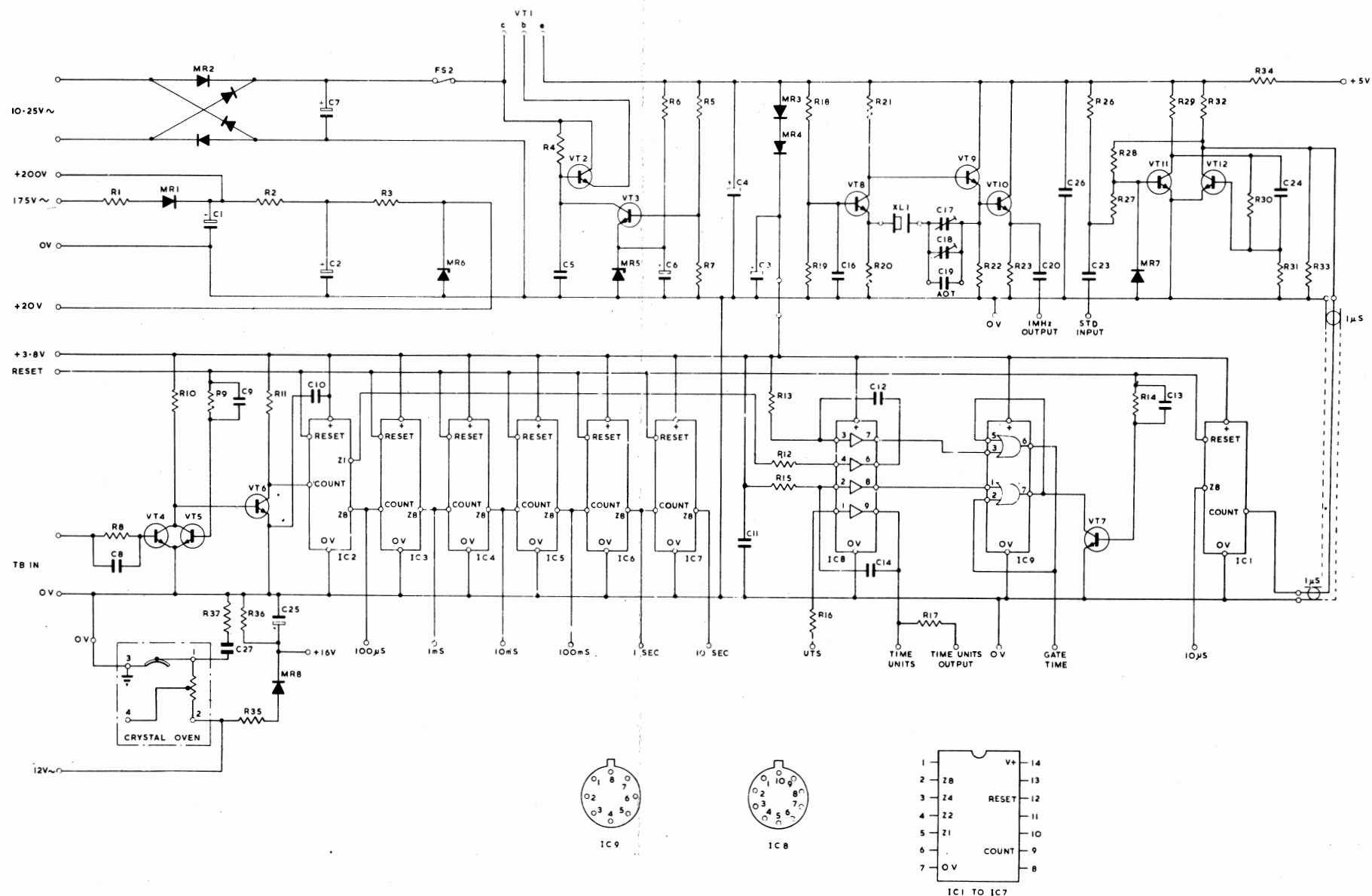




TOP VIEW OF INTEGRATED CIRCUITS

Fig. 2 Timebase and Power Supplies Circuit Diagram

Fig. 2 Timebase and Power Supplies Circuit Diagram

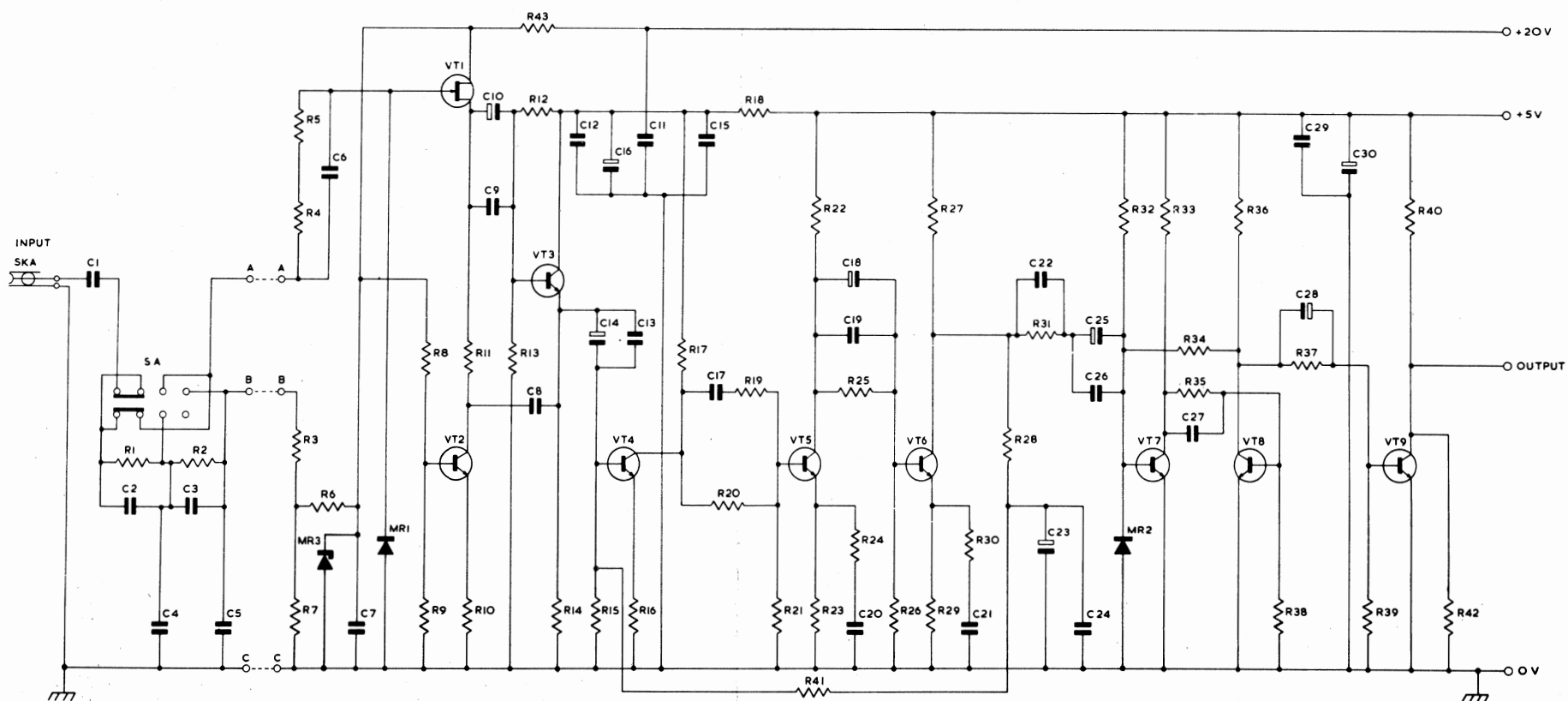


Fig. 3 Input Amplifier Circuit Diagram

Fig. 3 Input Amplifier Circuit Diagram

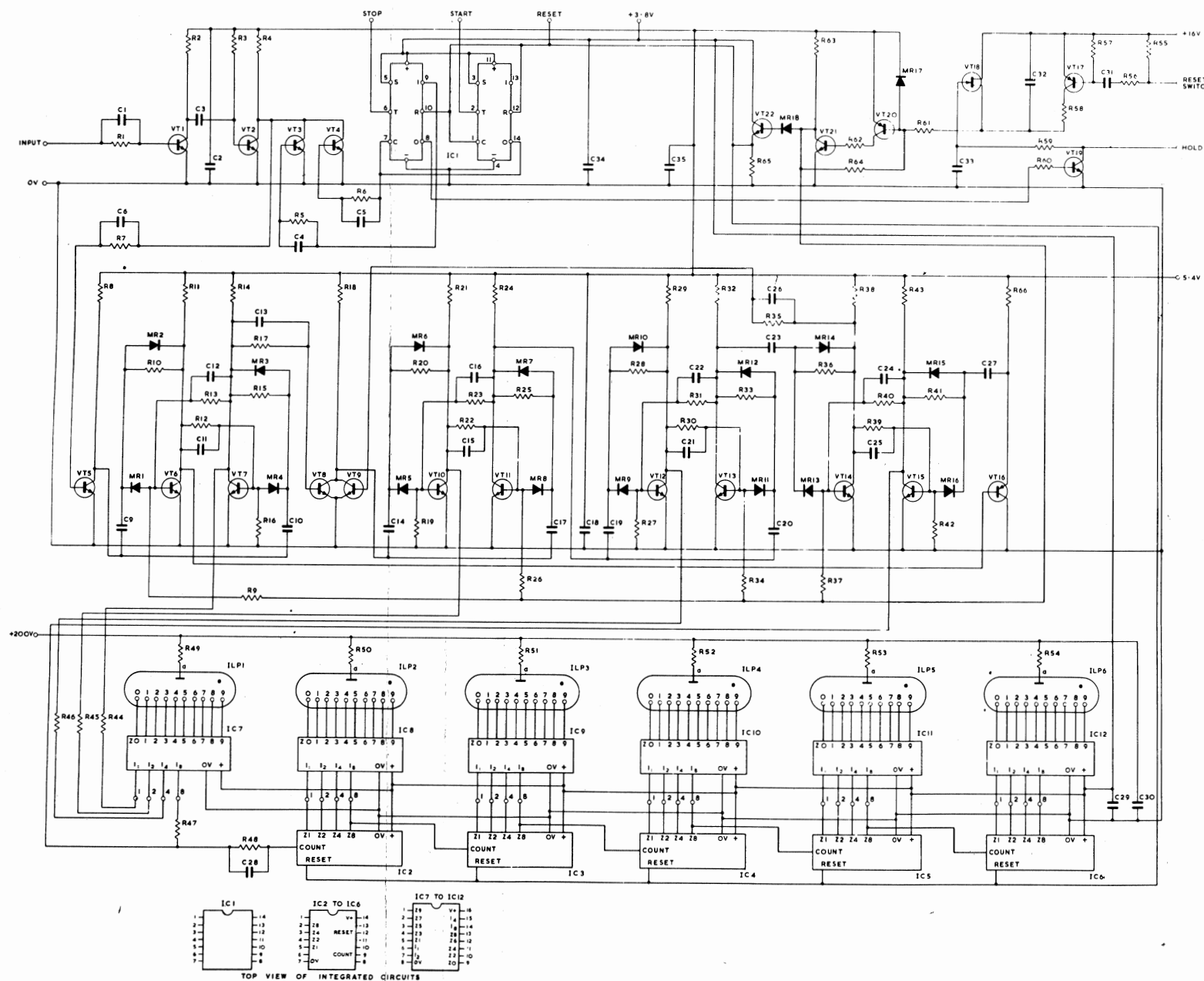


Fig. 4 Decade and Display Circuit Diagram

Fig. 4 Decade and Display Circuit Diagram

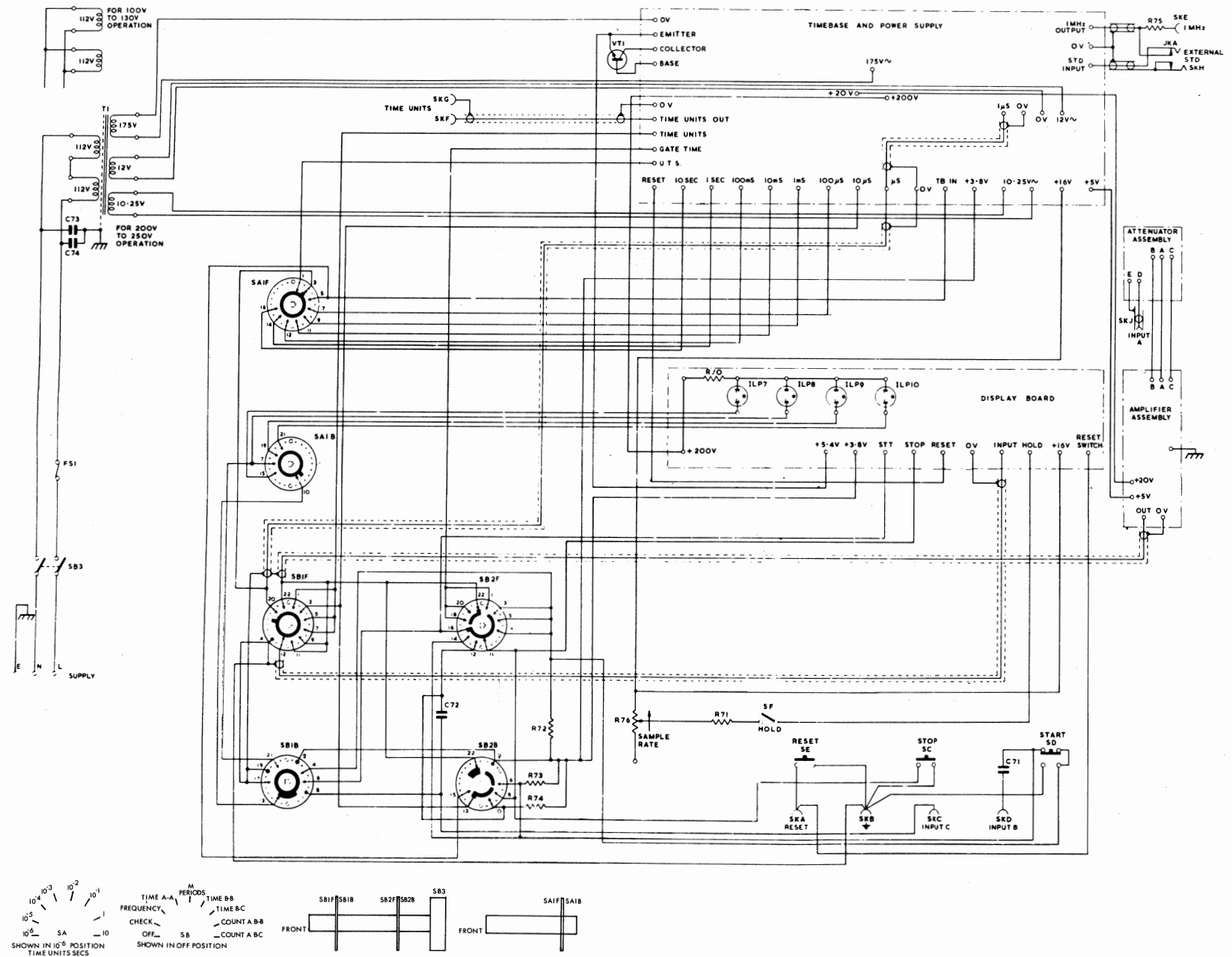


Fig. 5 Interconnection Diagram

Fig. 5 Interconnection Diagram