

LM317T (KA317) Adjustable Voltage Regulator (Positive)

3-TERMINAL POSITIVE ADJUSTABLE REGULATOR

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply 2.2A typical of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shutdown and safe area compensation.



- Output Current 2.2A Typical
- Output Adjustable Between 1. 2V and 37V
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current-Limiting
- Output Transistor Sate-Area Compensation
- TO-220 Package

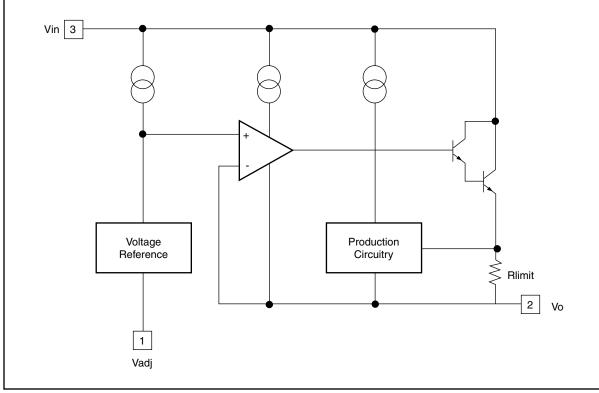


TO-220

ORDERING INFORMATION

Device	Package	Operating Temperature
LM317T (KA317)	TO-220	0°C ~ +125°C

BLOCK DIAGRAM



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ABSOLUTE MAXIMUM RATINGS (T_A= +25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Input-Output Voltage Differential	V _I - V _O	40	V
Lead Temperature	T _{LEAD}	230	°C
Power Dissipation	P _D	Internally limited	W
Operating Temperature Range	T _{OPR}	0 ~ +125	°C
Storage Temperature Range	T _{STG}	-65 ~ +125	°C
Temperature Coefficient of Output Voltage	V _O /T	0.02	%/°C

ELECTRICAL CHARACTERISTICS

 $(V_I - V_O = 5V, \, I_O = 0.5A, \, 0^{\circ}C \leq T_J \leq +125^{\circ}C, \, I_{MAX} = 1.5A, \, P_{MAX} = 20W, \, unless \, otherwise \, specified)$

Characteristic	Symbol	Te	st Conditions	Min	Тур	Max	Unit
Line Regulation	Rline	T _A = +25°C	$3V \le V_I - V_O \le 40V$		0.01	0.04	%/V
		7	$3V \le V_1 - V_0 \le 40V$		0.02	0.07	%/V
Load Regulation	Rload	$T_A = +25^{\circ}C, 1$	$0mA \le I_O \le I_{MAX}$				
_		V _O < 5V	J		18	25	mV
		$V_O \ge 5V$			0.4	0.5	%/V _O
		$10mA \le I_O \le I_{MAX}$					
		$V_O < 5V$			40	70	mV
		$V_O \ge 5V$			0.8	1.5	%/V _O
Adjustable Pin Current	I_{ADJ}				46	100	μA
Adjustable Pin Current Change	ΔI_{ADJ}	$3V \le V_I - V_O \le$	40V		2.0	5	μA
	$10mA \le I_O \le I_{MAX}$						
		$P \le P_{MAX}$					
Reference Voltage	V_{REF}	$3V \le V_{IN} - V_{OUT} \le 40V$		1.20	1.25	1.30	V
		$10\text{mA} \le I_{O} \le I_{N}$	MAX				
		$P_D \le P_{MAX}$					
Temperature Stability	ST _t				0.7		%/V _O
Minimum Load Current to Maintain Regulation	$L_{(MIN)}$	V _I - V _O = 40V			3.5	12	mA
Maximum Output Current		V V < 15V B < B		1.0	2.2		Α
Maximum Output Ourient	I _{O(MAX)}	$V_{I} - V_{O} \le 15V, P_{D} \le P_{MAX}$ $V_{I} - V_{O} \le 40V, P_{D} \le P_{MAX}, T_{A} = 25^{\circ}C$		1.0	0.3		_ ^
RMS Noise, % of V _{OUT}	e _N	$T_A = +25^{\circ}C$, $10Hz \le f \le 10KHz$			0.003	0.01	%/V _O
Ripple Rejection	RR	, , , , , , , , , , , , , , , , , , ,					dB
Tupple Hejeotion		without C _{AD.1}	120112		60		45
		$C_{ADJ} = 10\mu F$		66	75		
Long-Term Stability, T _J = T _{HIGH}	ST	$T_A = +25^{\circ}C$ for	r end point		0.3	1	%
5 77 5 man		measurements					
Thermal Resistance Junction to Case	$R_{\theta JC}$				5		°C/W

Load and line regulation are specified at constant junction temperature. Change in V_D due to heating effects must be taken into account separately. Pulse testing with low duty is used. (P_{MAX} = 20W)

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TYPICAL PERFORMANCE CHARACTERISTICS

Fig. 1 Load Regulation

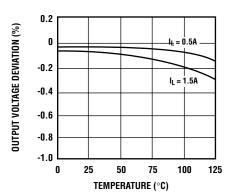


Fig. 2 Adjustment Current

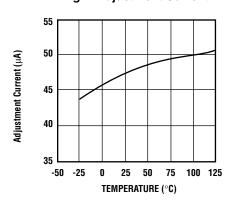


Fig. 3 Dropout Voltage

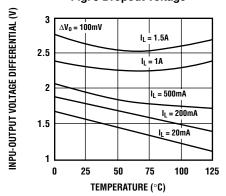
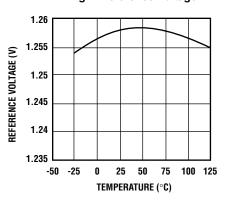
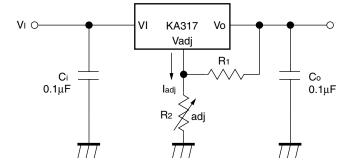


Fig. 4 Reference Voltage



Typical Application

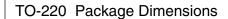


 $Vo = 1.25V (1 + R_2/R_1) + Iadj R_2$

Fig. 5 Programmable Regulator

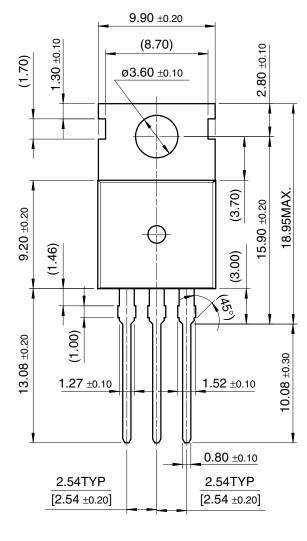
C_i is required when regulator is located at an appreciable distance from the power supply filter.

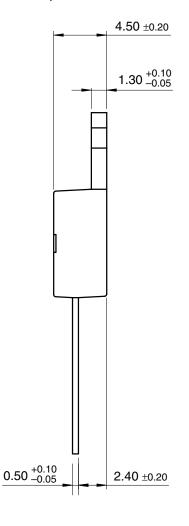
 C_0 improves transient response by reducing AC noise which is present at the output. Since I ADJ is controlled to less than 100 μ A, the error associated with this term is negligible in most applications.

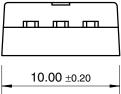




TO-220 (FS PKG CODE AE)







Dimensions in Millimeters

August 1999, Rev B

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