

UNREGULATED INPUT  
+VE

REGULATED OUTPUT  
+VE

ALL RESISTORS  $\frac{1}{4}$  W  
EXCEPT R11 & R15.

PRINTED  
CIRCUIT  
BOARD

TR2  
2N3772  
(EXTERNAL)

TR1  
40409

R11  
 $\frac{1}{2}$  W

R15  
270  
 $\frac{1}{2}$  W

R10

R12

R7

R6

IC1  
 $\mu$ A723

R5

R4

R3

R8

R9

R13

R14

C1  
100 $\mu$   
40V

C2  
16 $\mu$   
10V

C3  
4.7n

C4  
16 $\mu$   
40V

D1  
39V

S R P

N

M

H

J

K

L

D

E

F

+VE

TITLE

UNIVERSAL  
VOLTAGE REGULATOR

ORIGIN L. G. SMITH

DATE 10-7-70

SUB TITLE

DRAWN J. D. HURREN

14-7-70

APPROVED

CHKD J. D. HURREN

14-7-70

CIRCUIT DIAGRAM

A4

STATION No /

HEAD OFFICE No

10.084/4